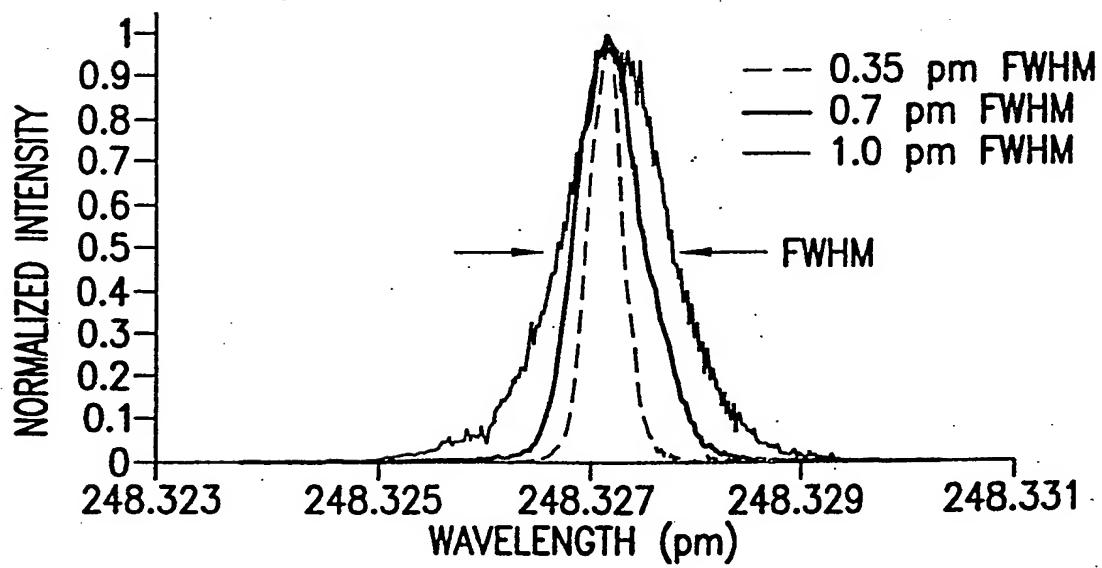


FIG. 1A

FIG. 1B



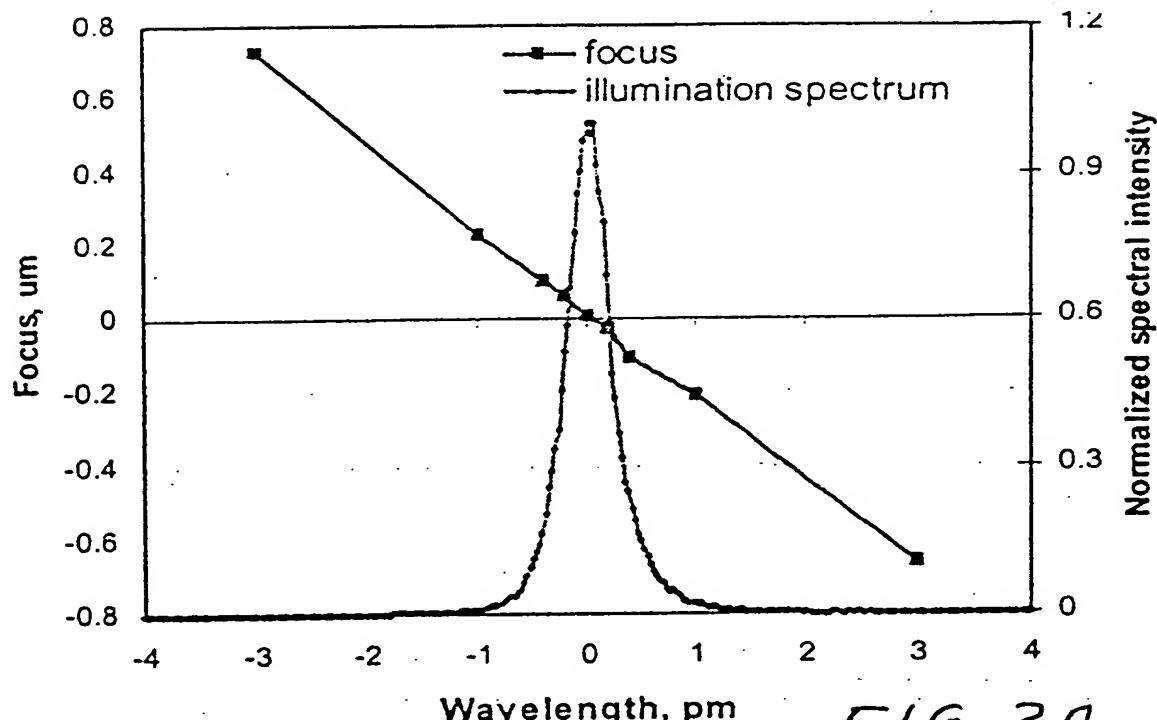


FIG. 2A

FIG. 2B

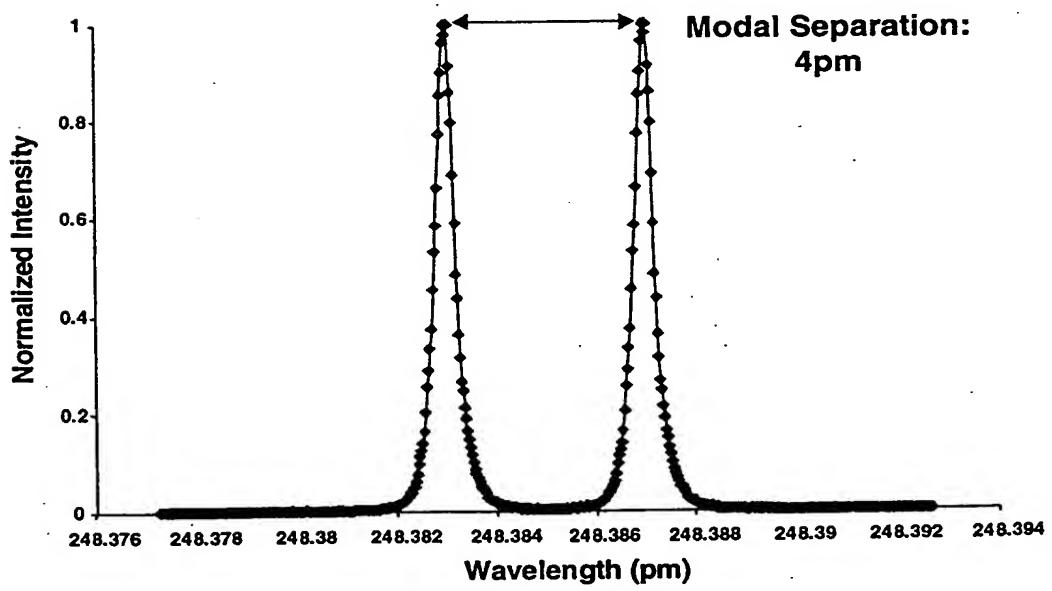
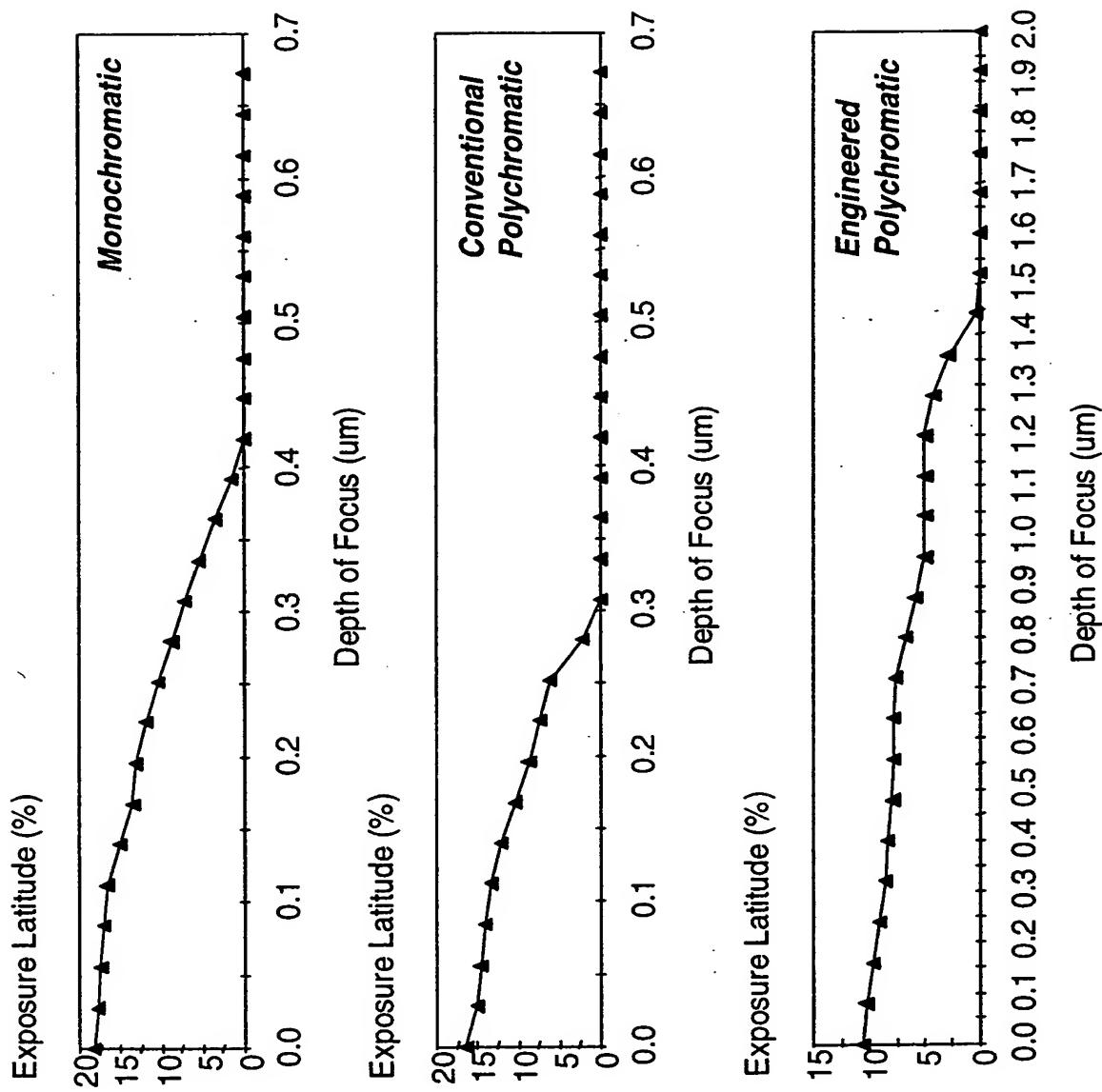
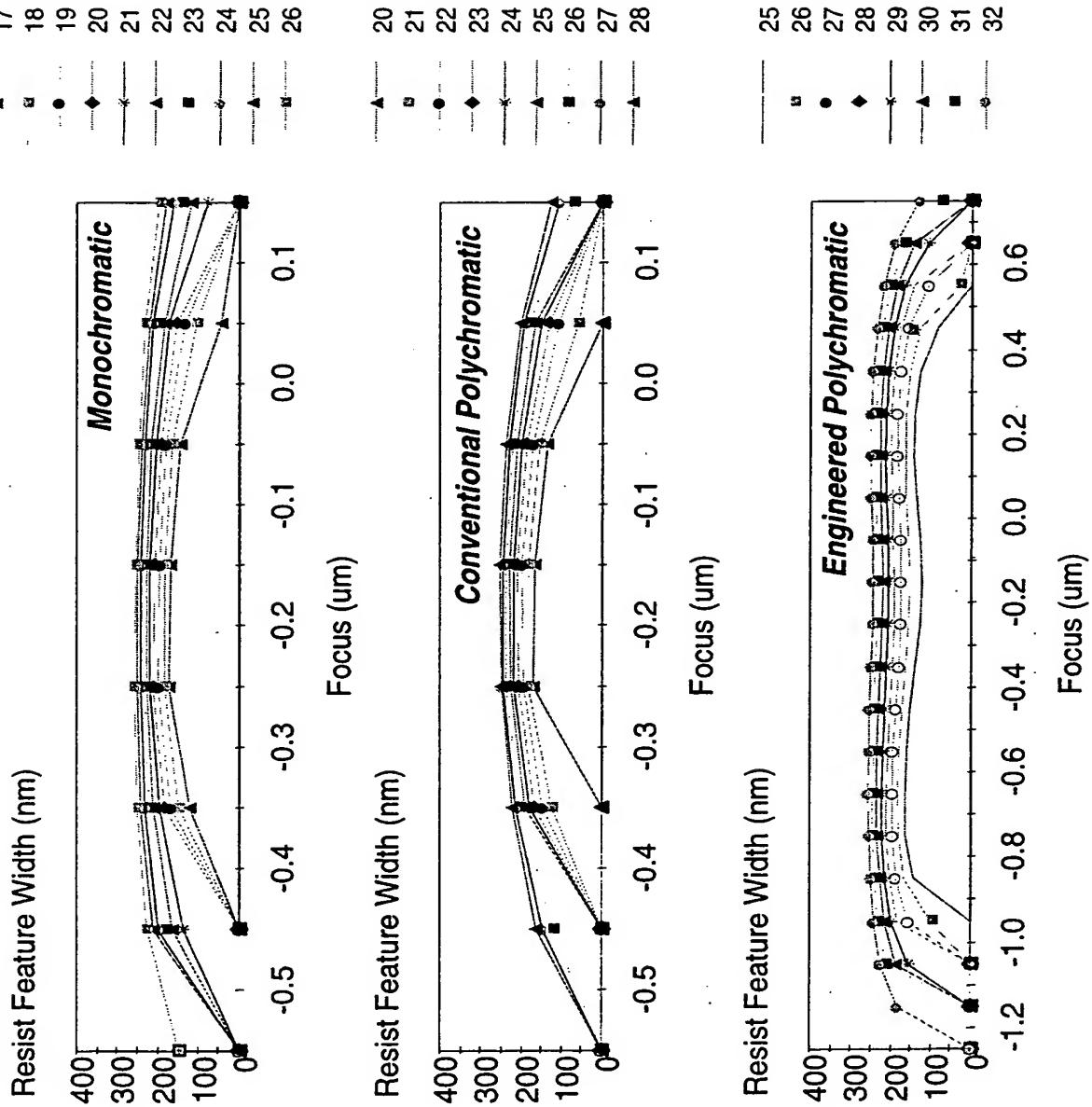


FIG. 2D





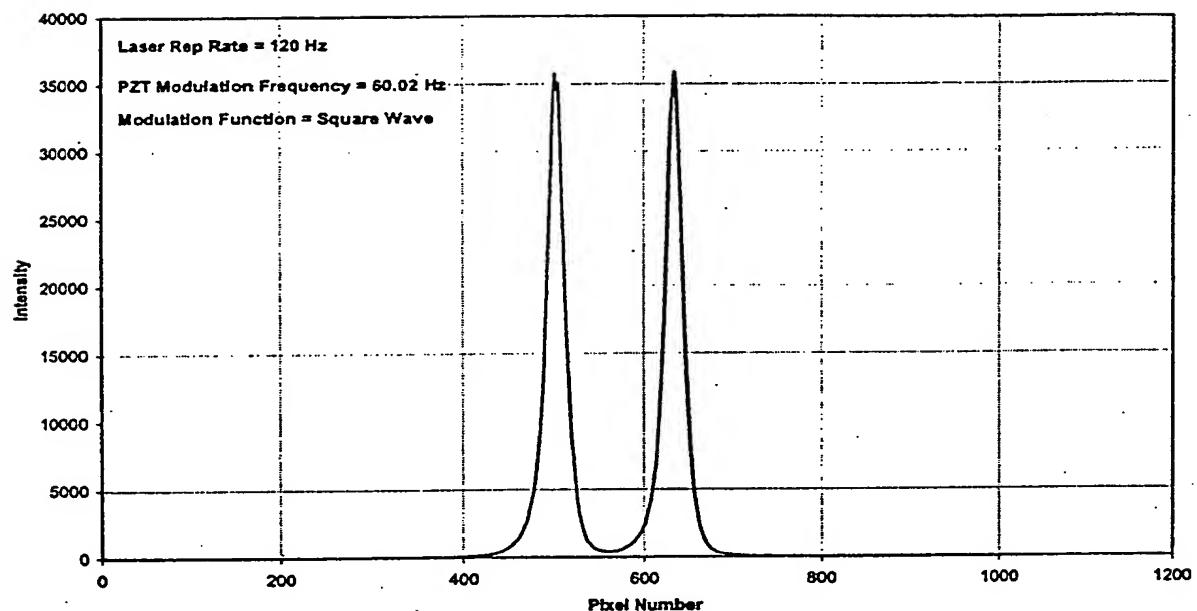


FIG. 2E

FIG. 2F

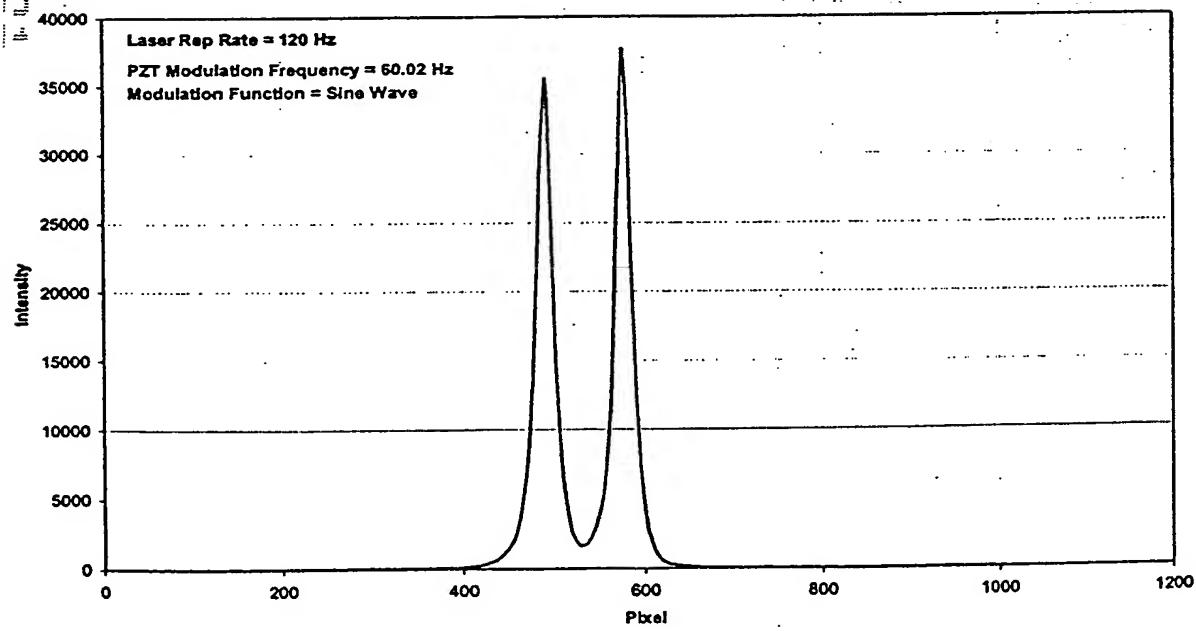


FIG. 2 G 1

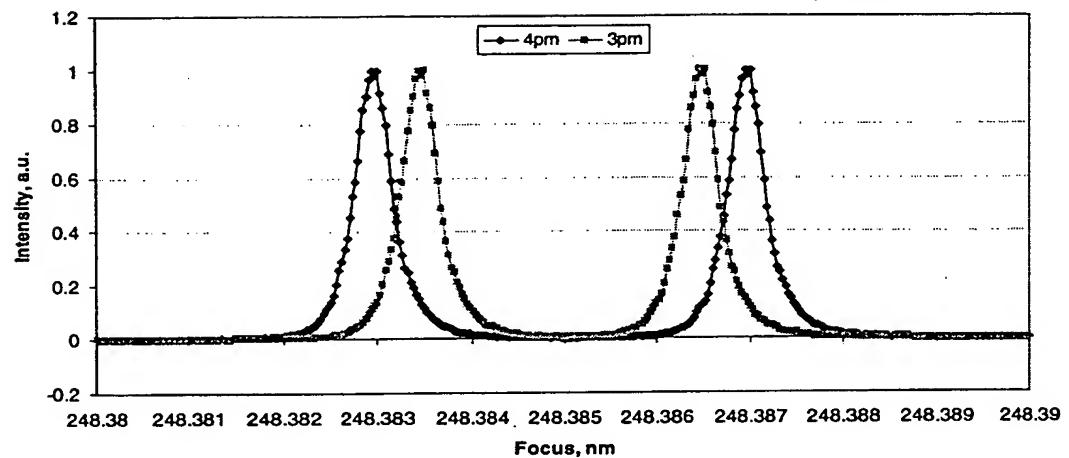


FIG. 2 G 2

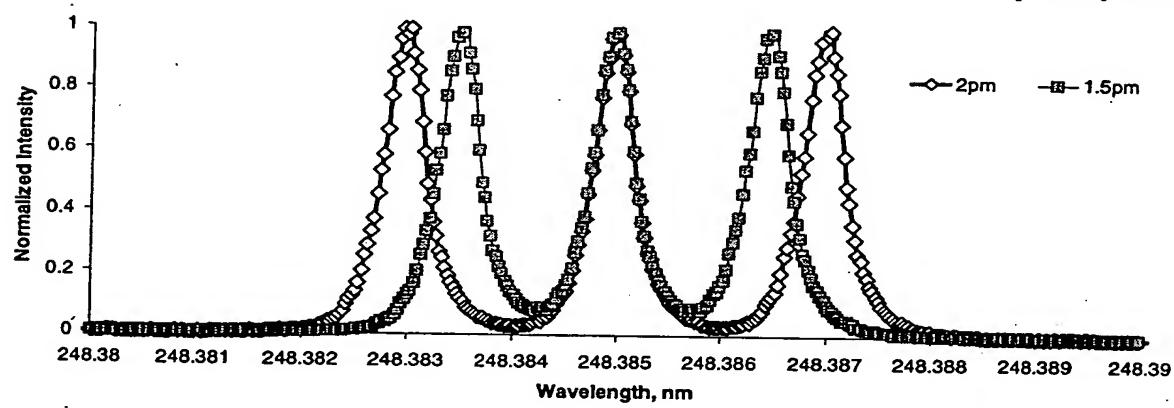
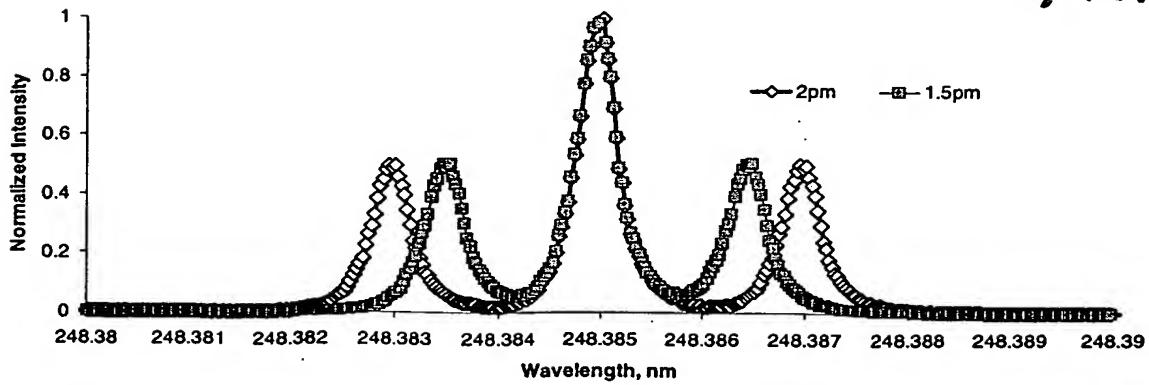


FIG. 2 G 3



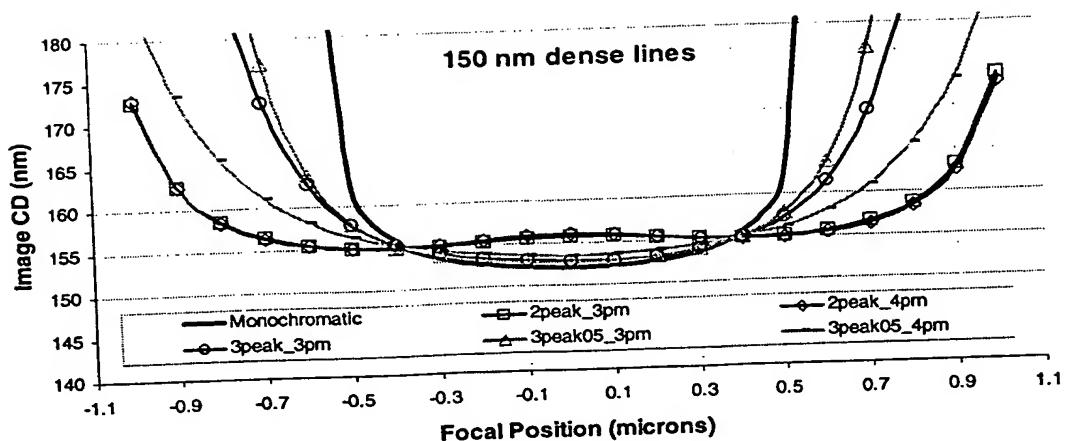


FIG. 2H1

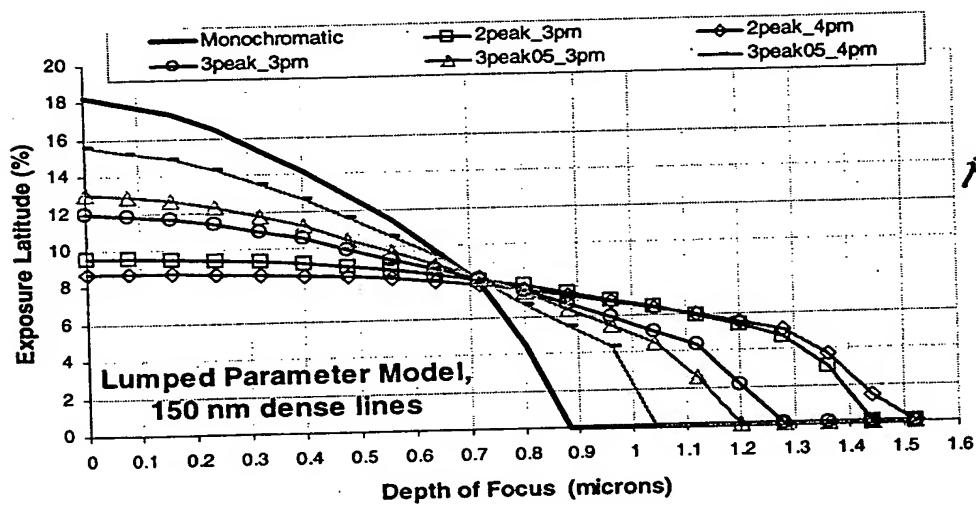


FIG. 2H2

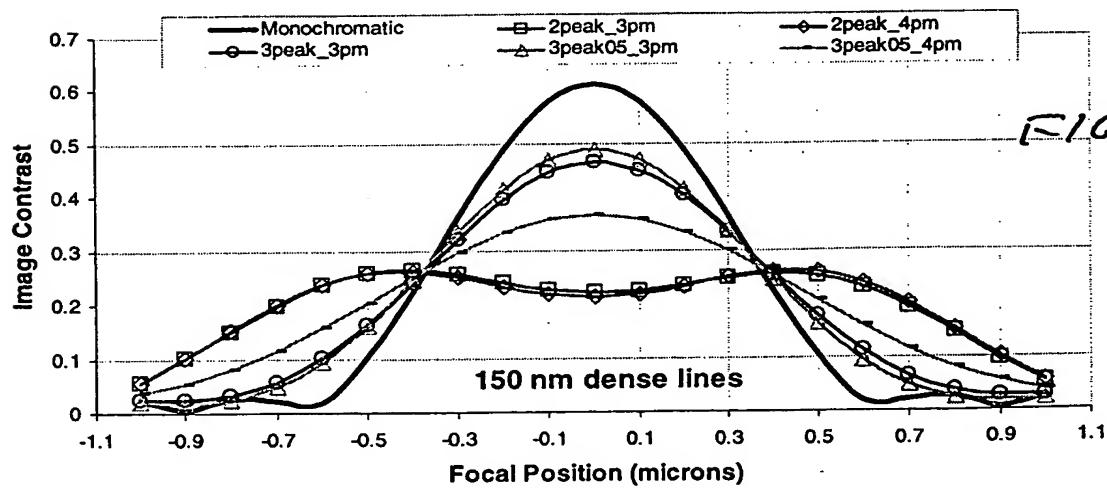
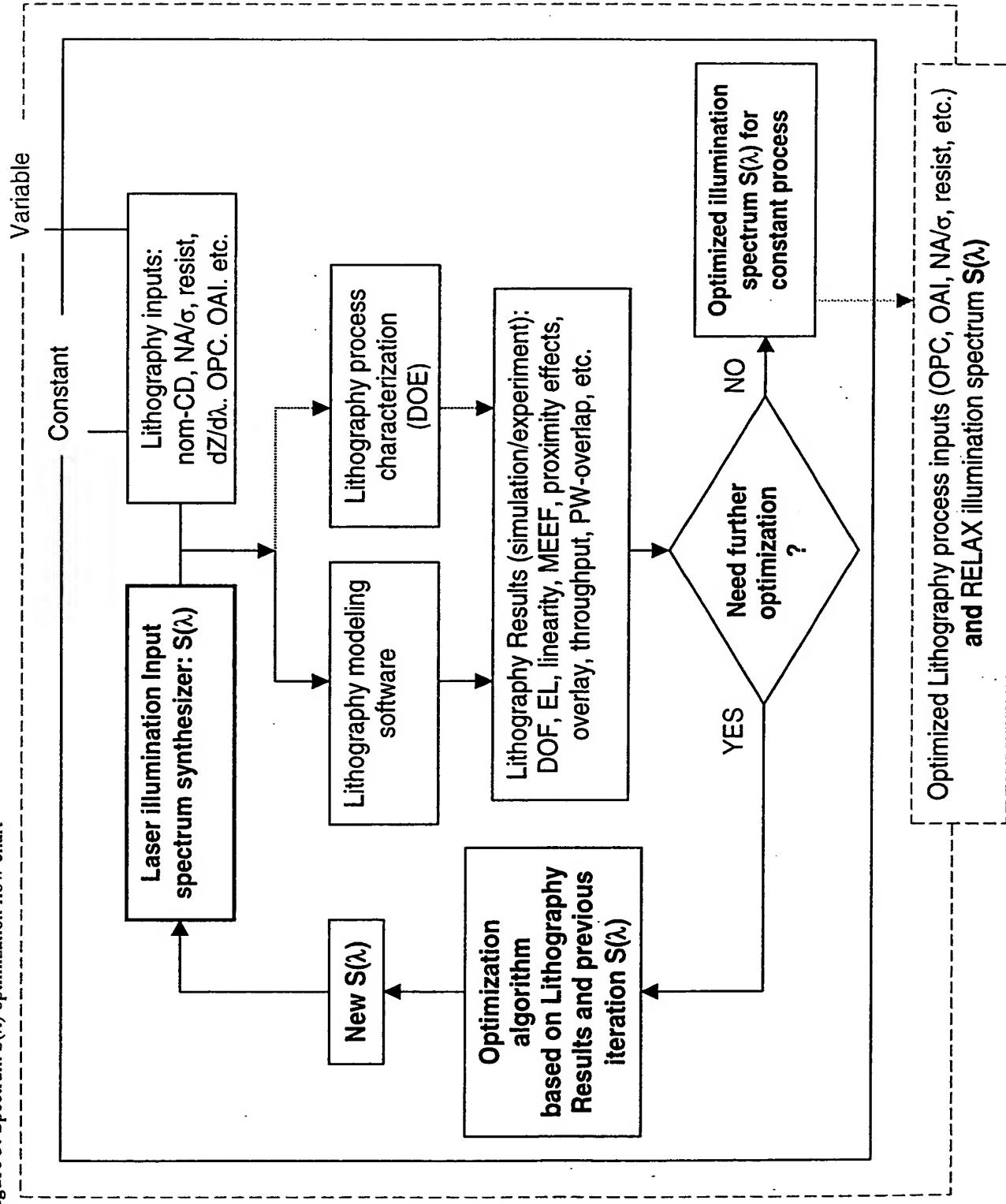


FIG. 2H3

FIG. 2I

Figure 3: Spectrum $S(\lambda)$ optimization flow-chart



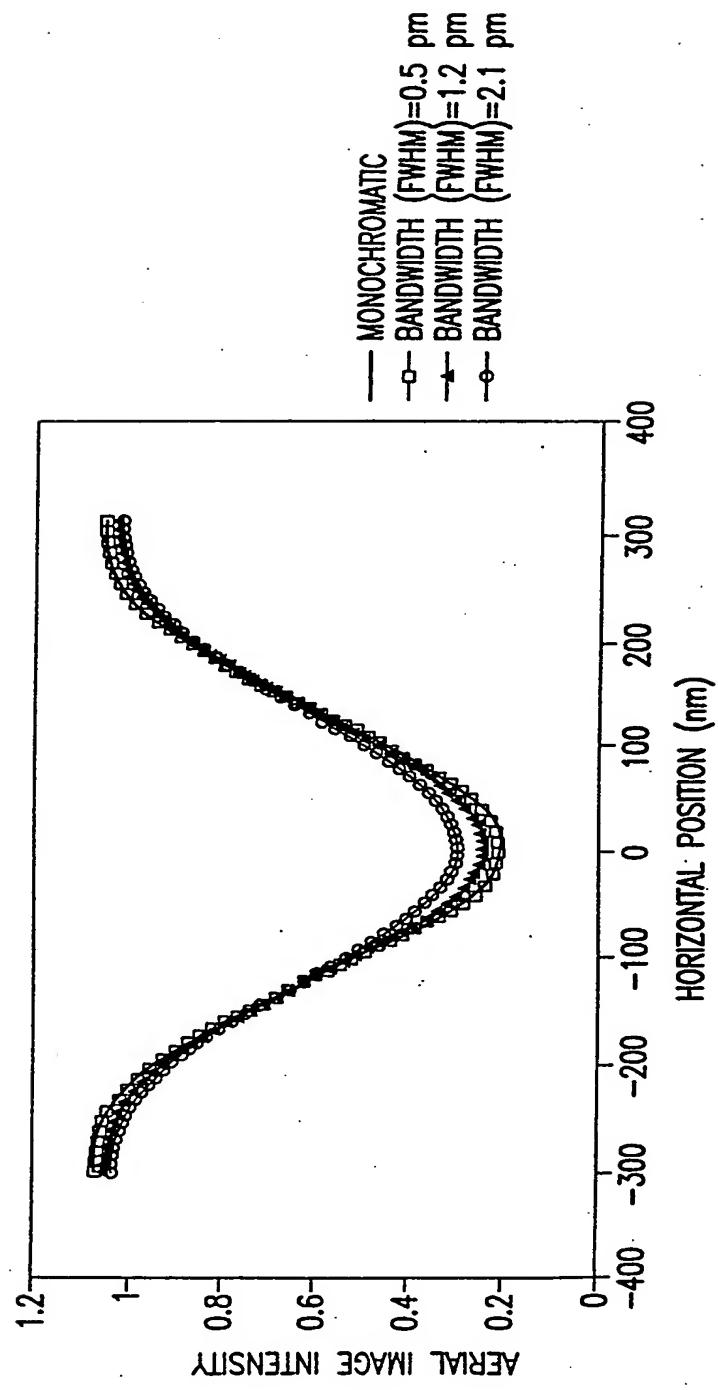


FIG. 3

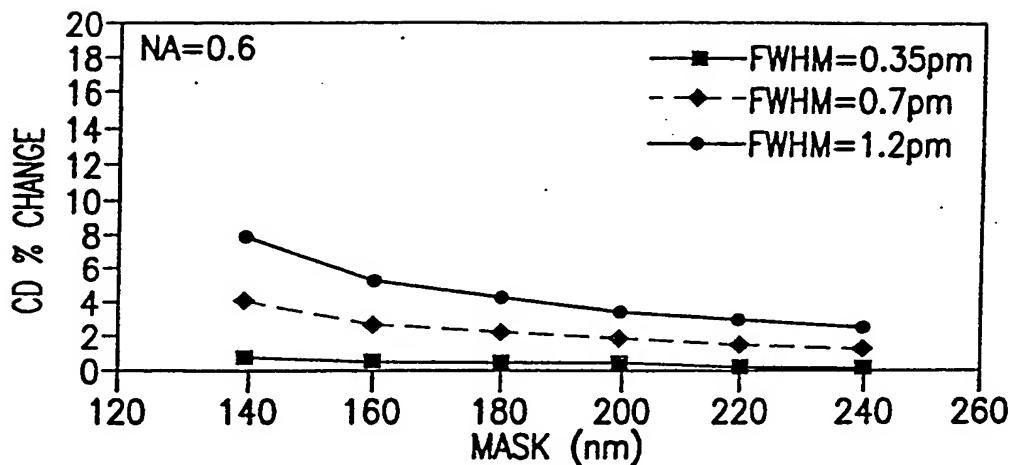


FIG.4A

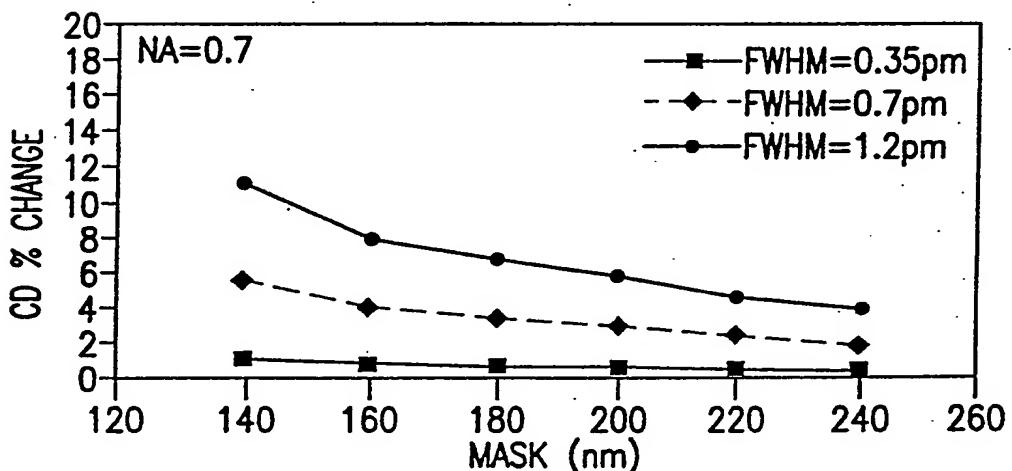


FIG.4B

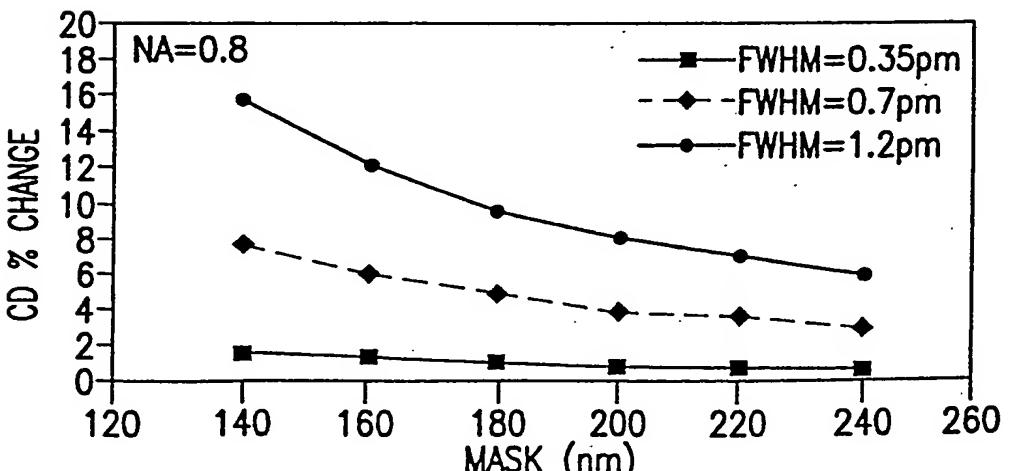


FIG 4C

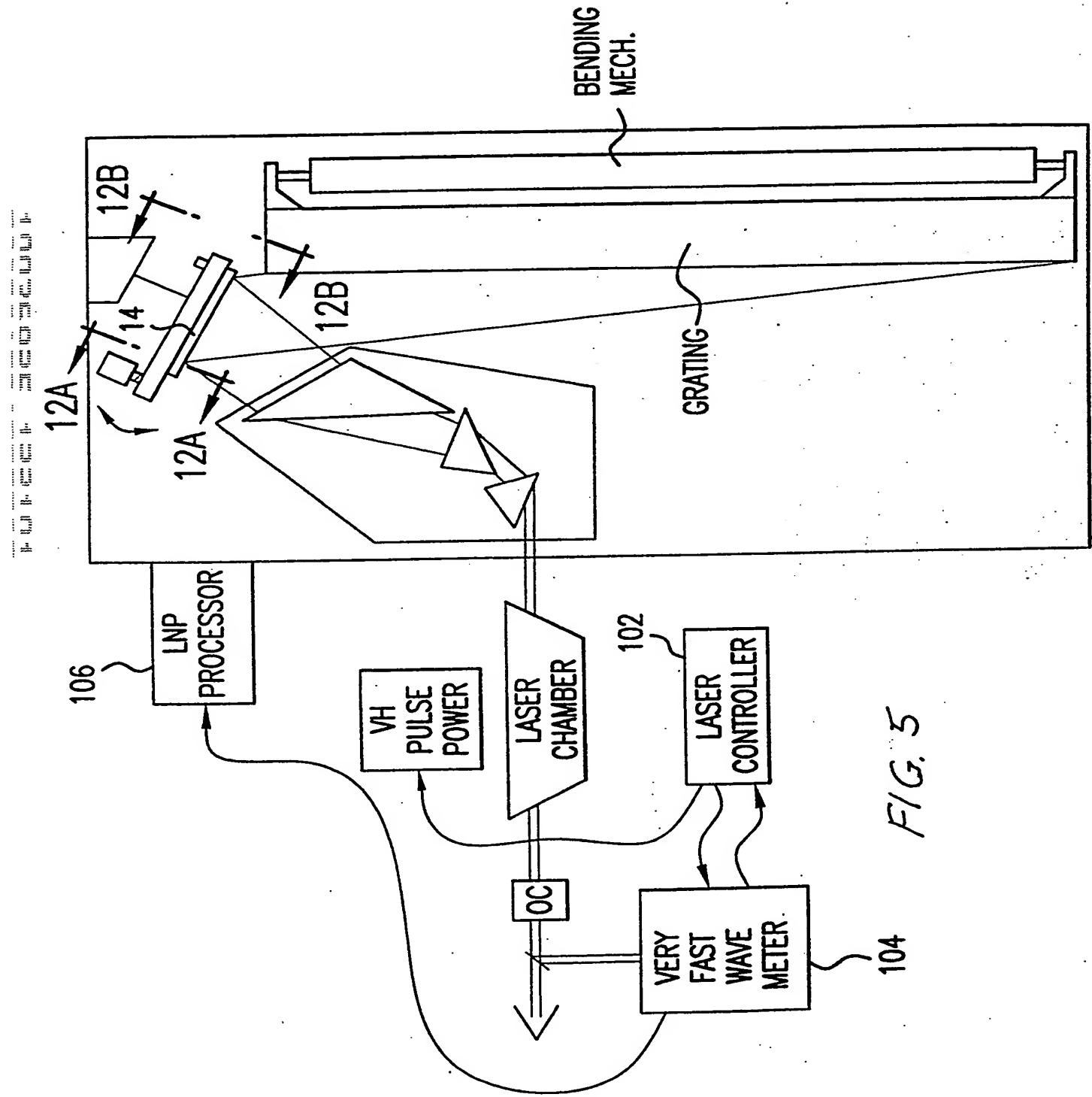


FIG. 5

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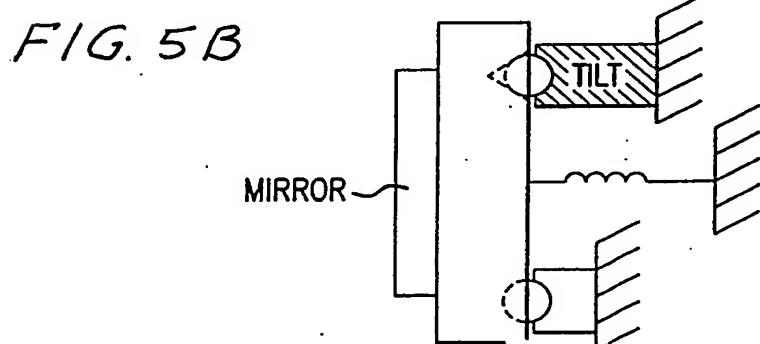
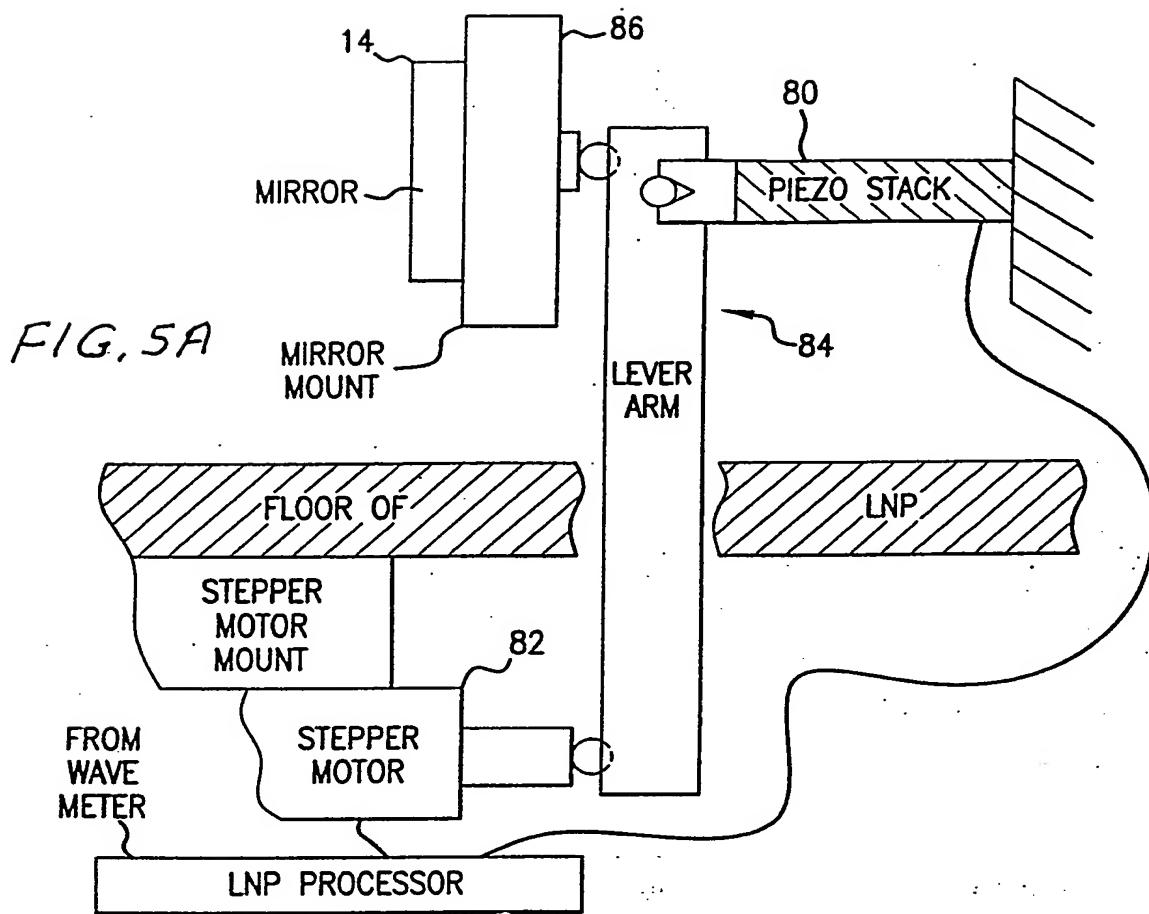
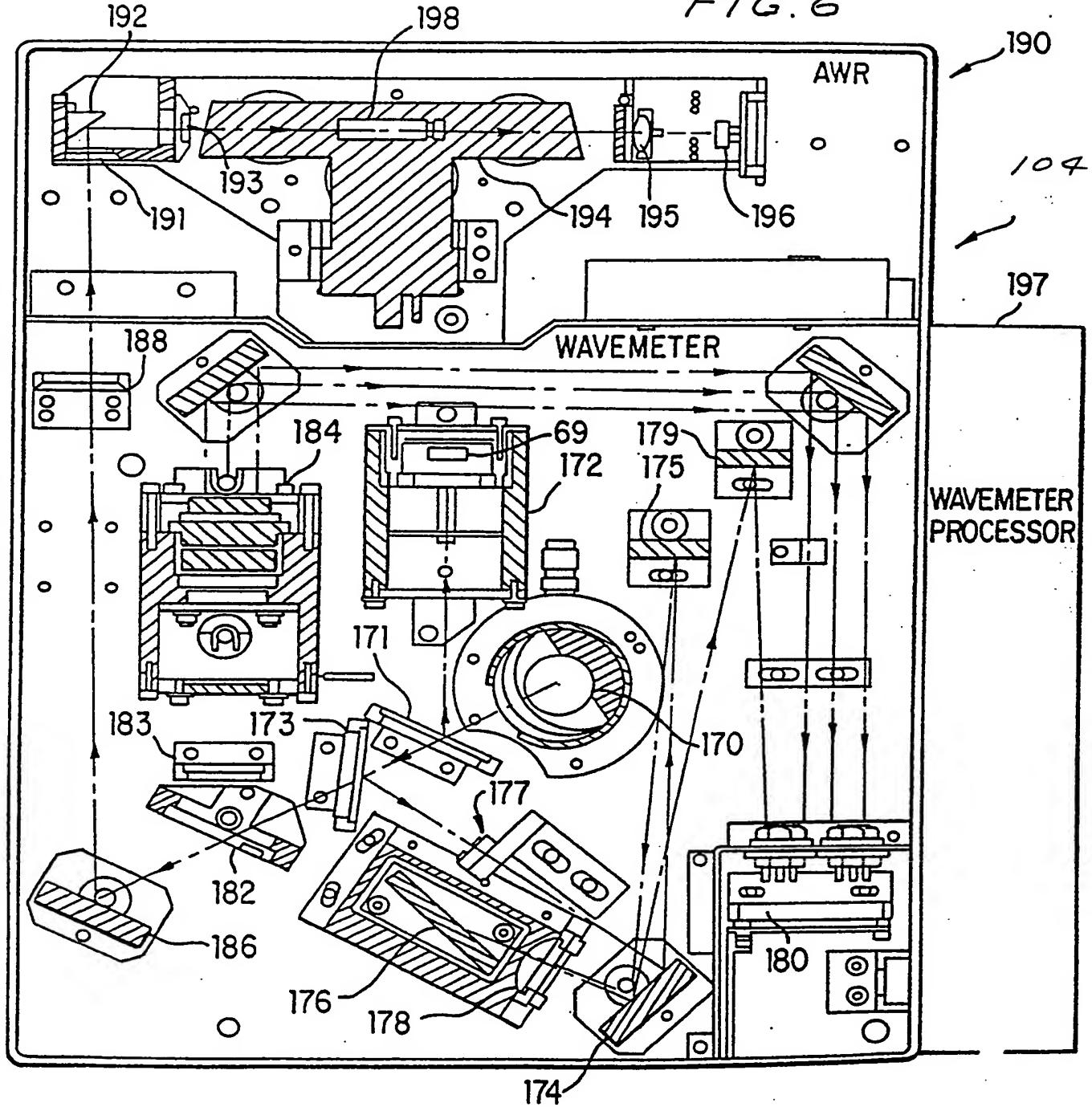


FIG. 6



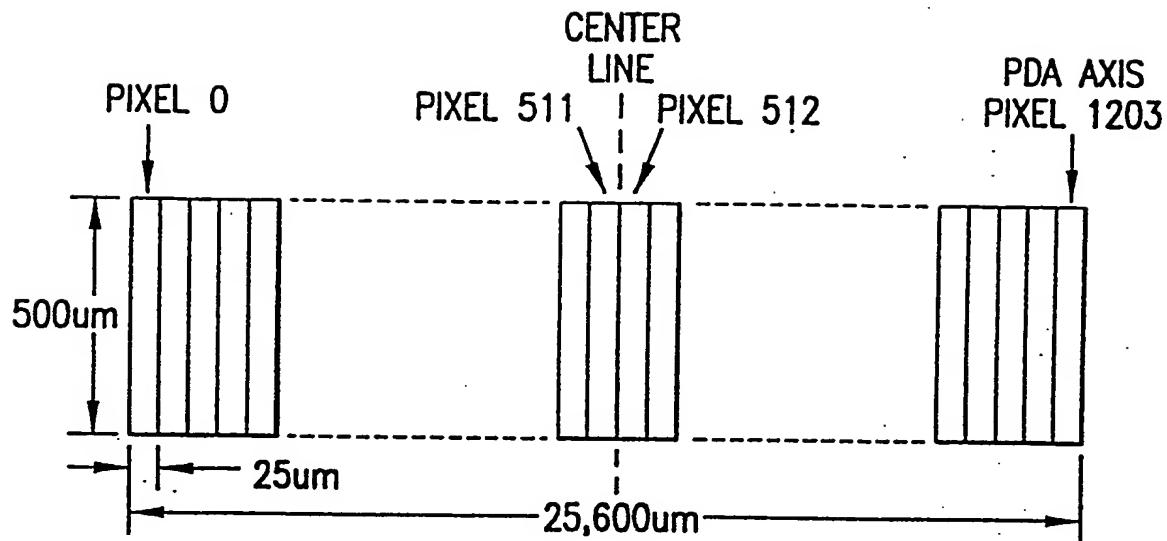


FIG. 6A

FIG. 6B

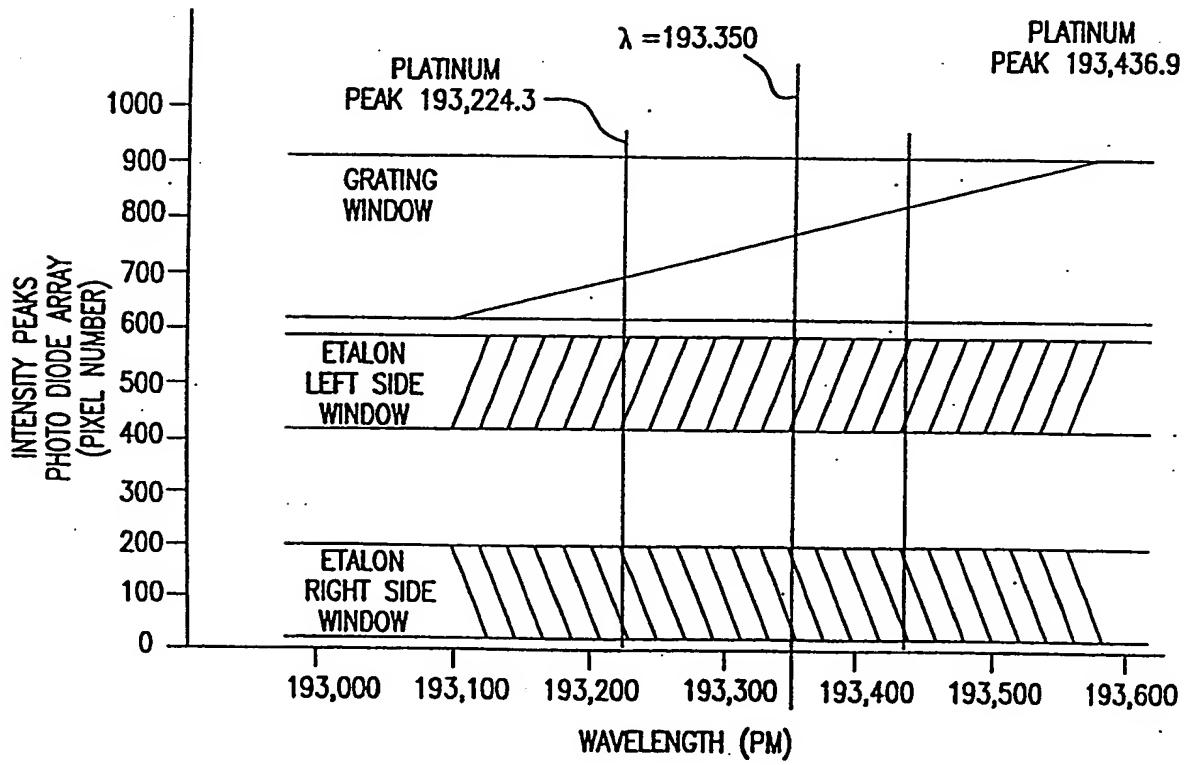
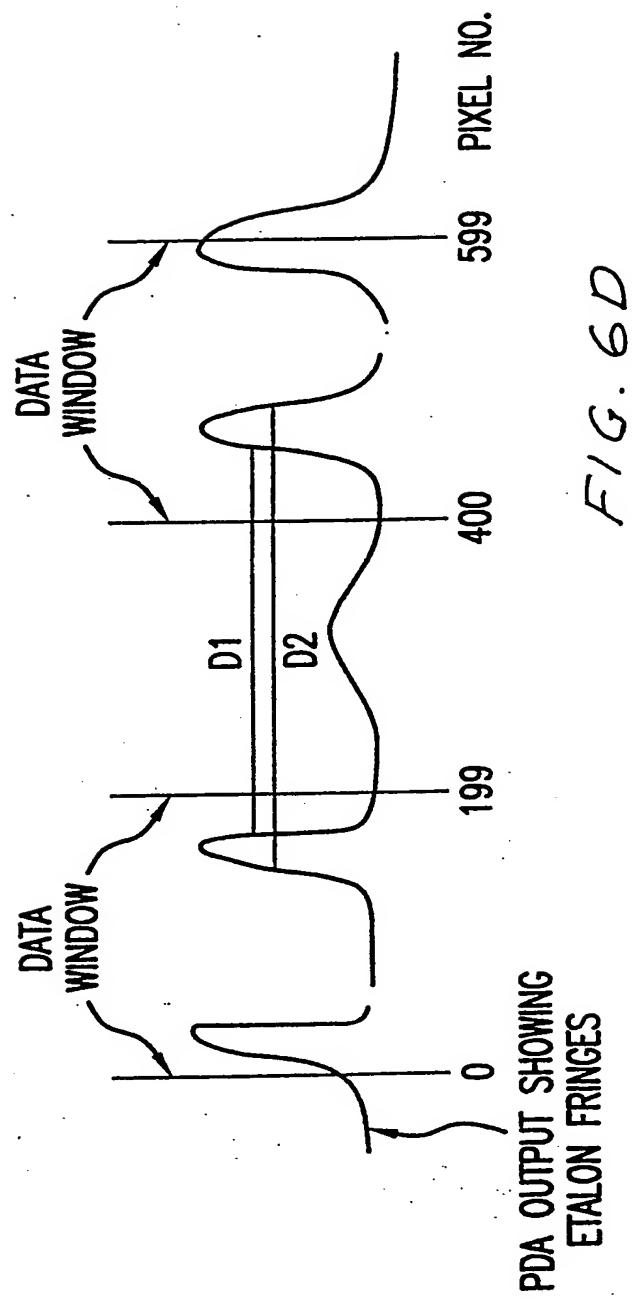
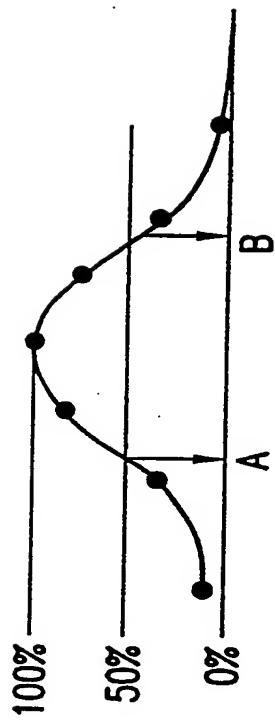


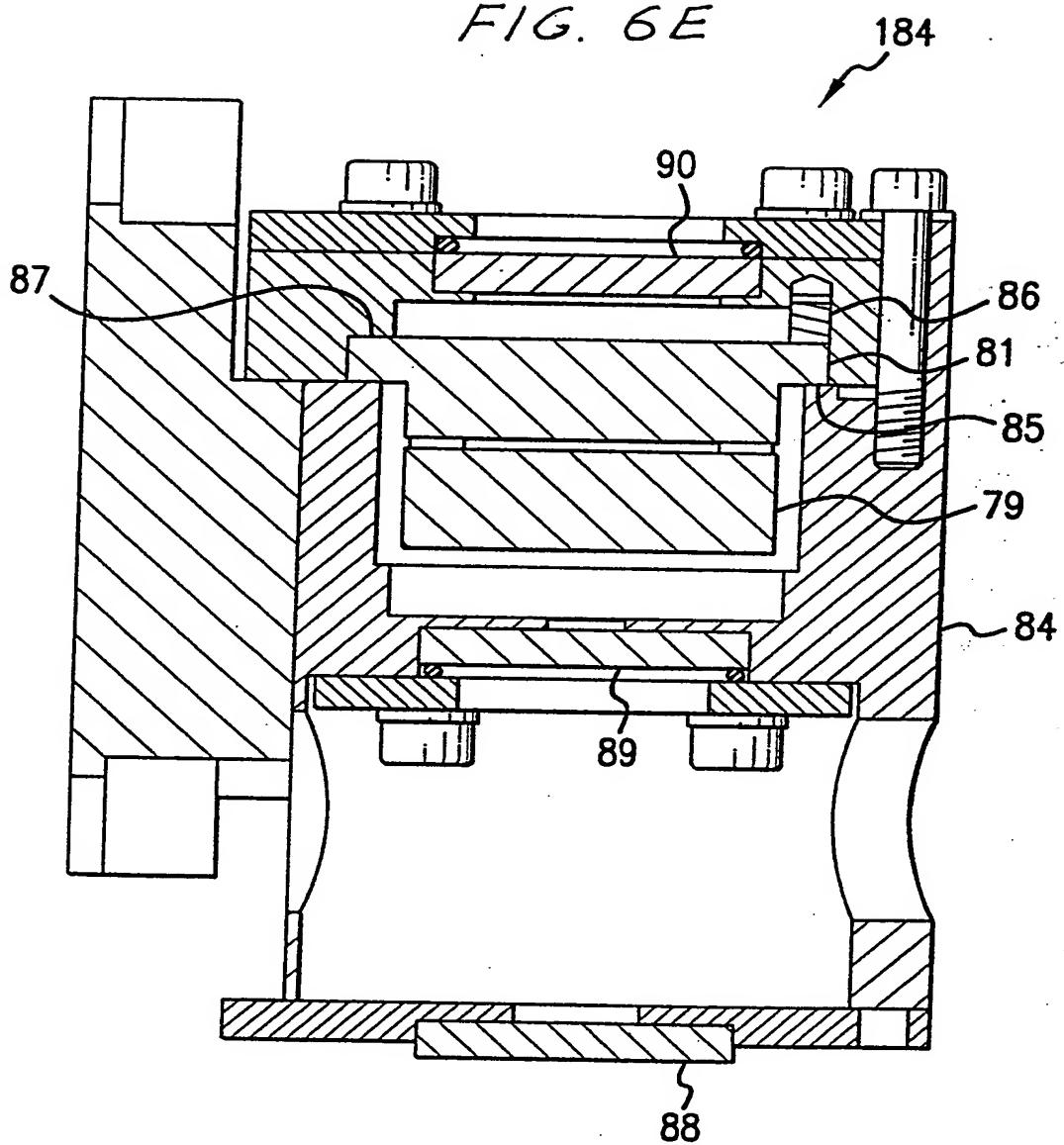
FIG. 6C



PDA OUTPUT SHOWING
ETALON FRINGES

FIG. 6D

FIG. 6E



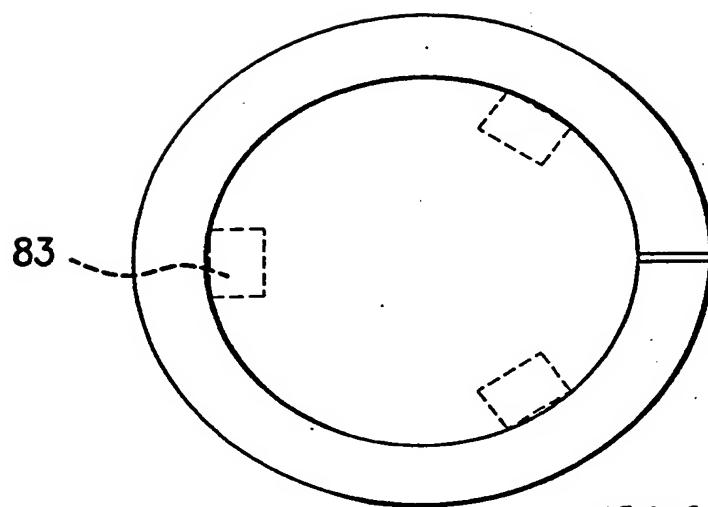


FIG. 6F

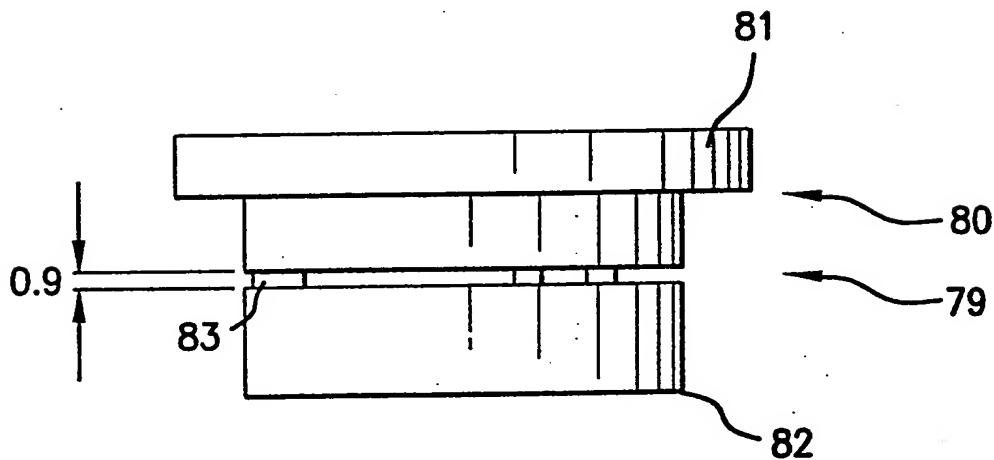


FIG. 6G

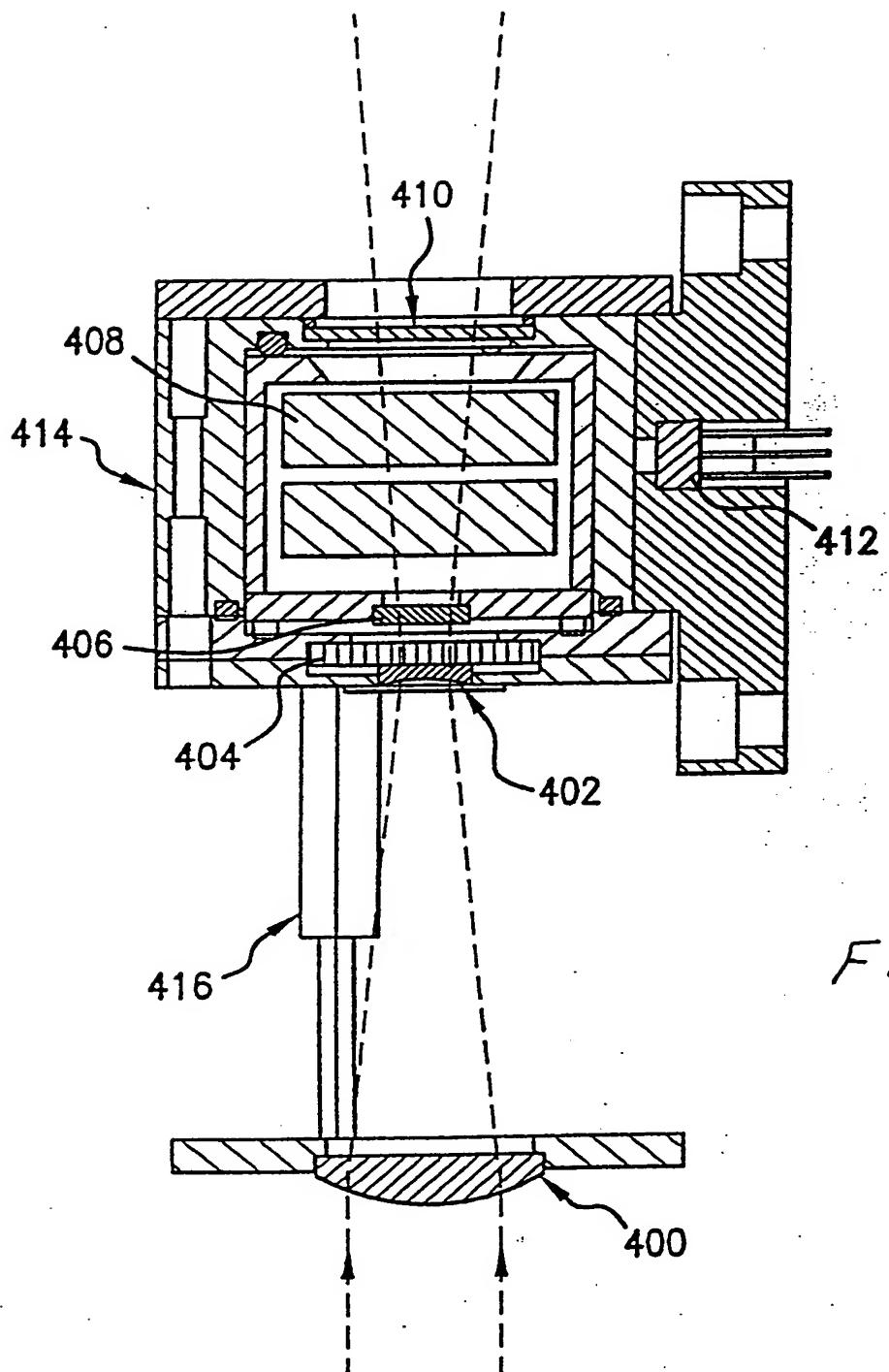
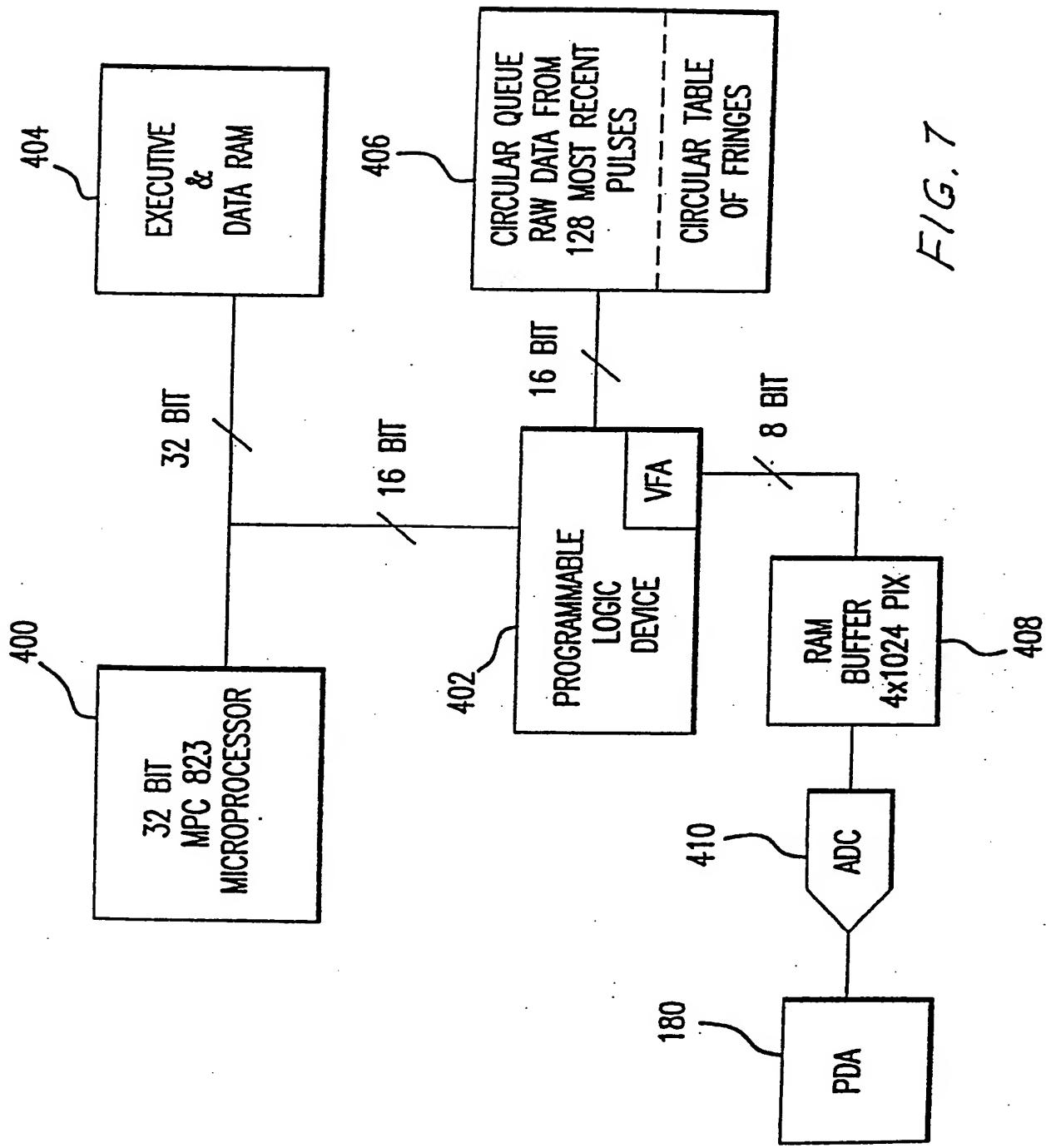


FIG. 6H



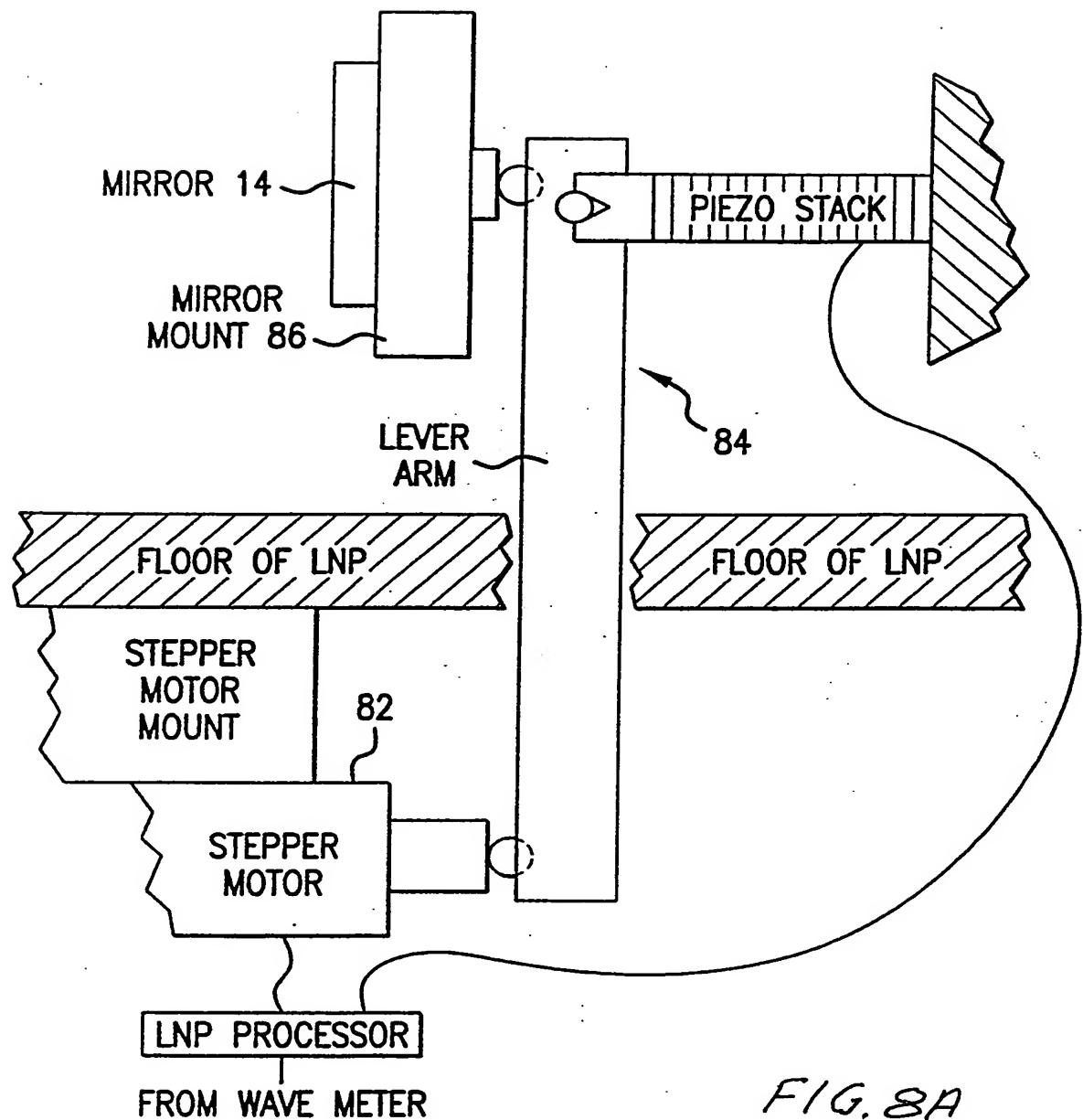
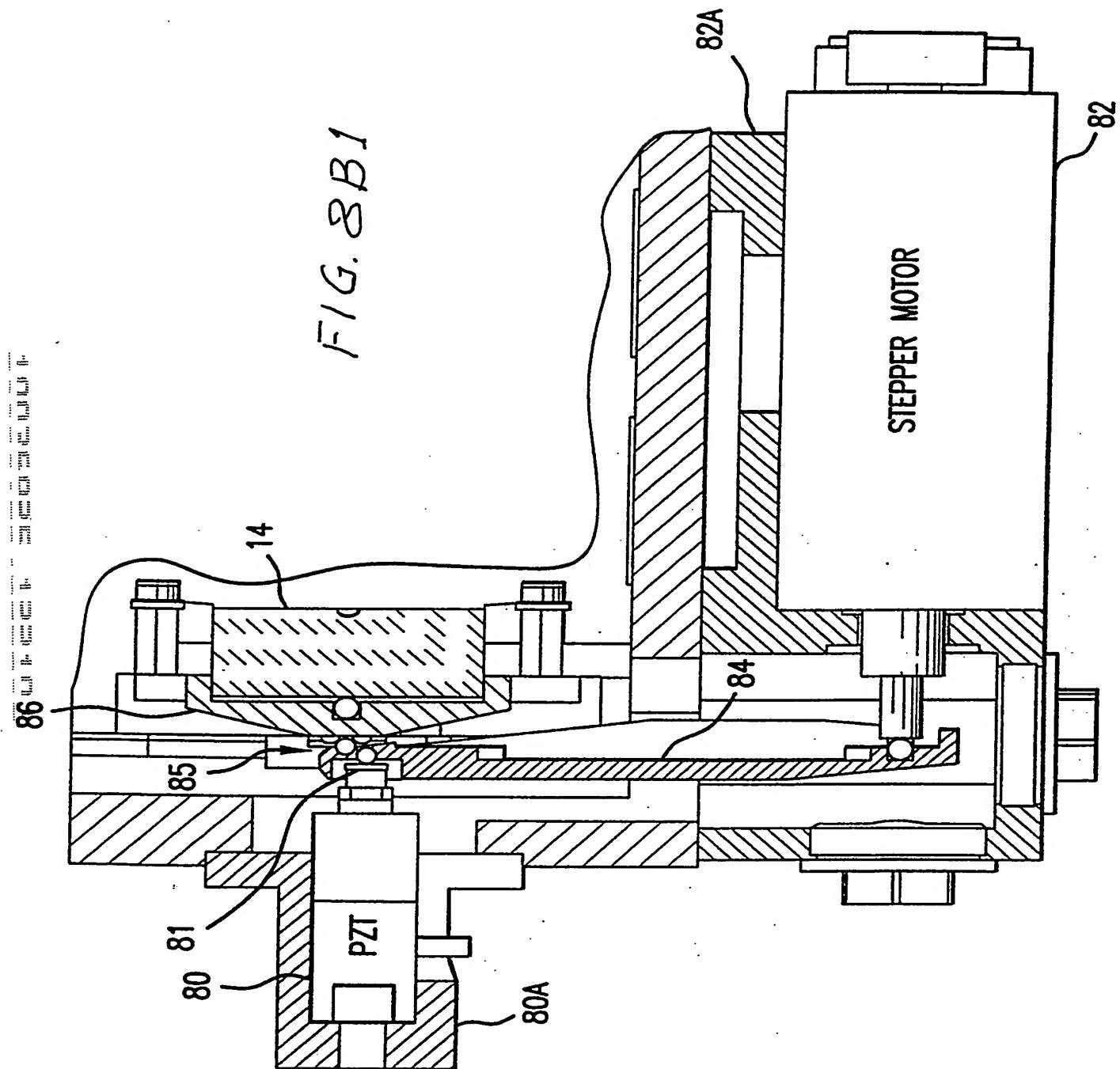


FIG. 8B1



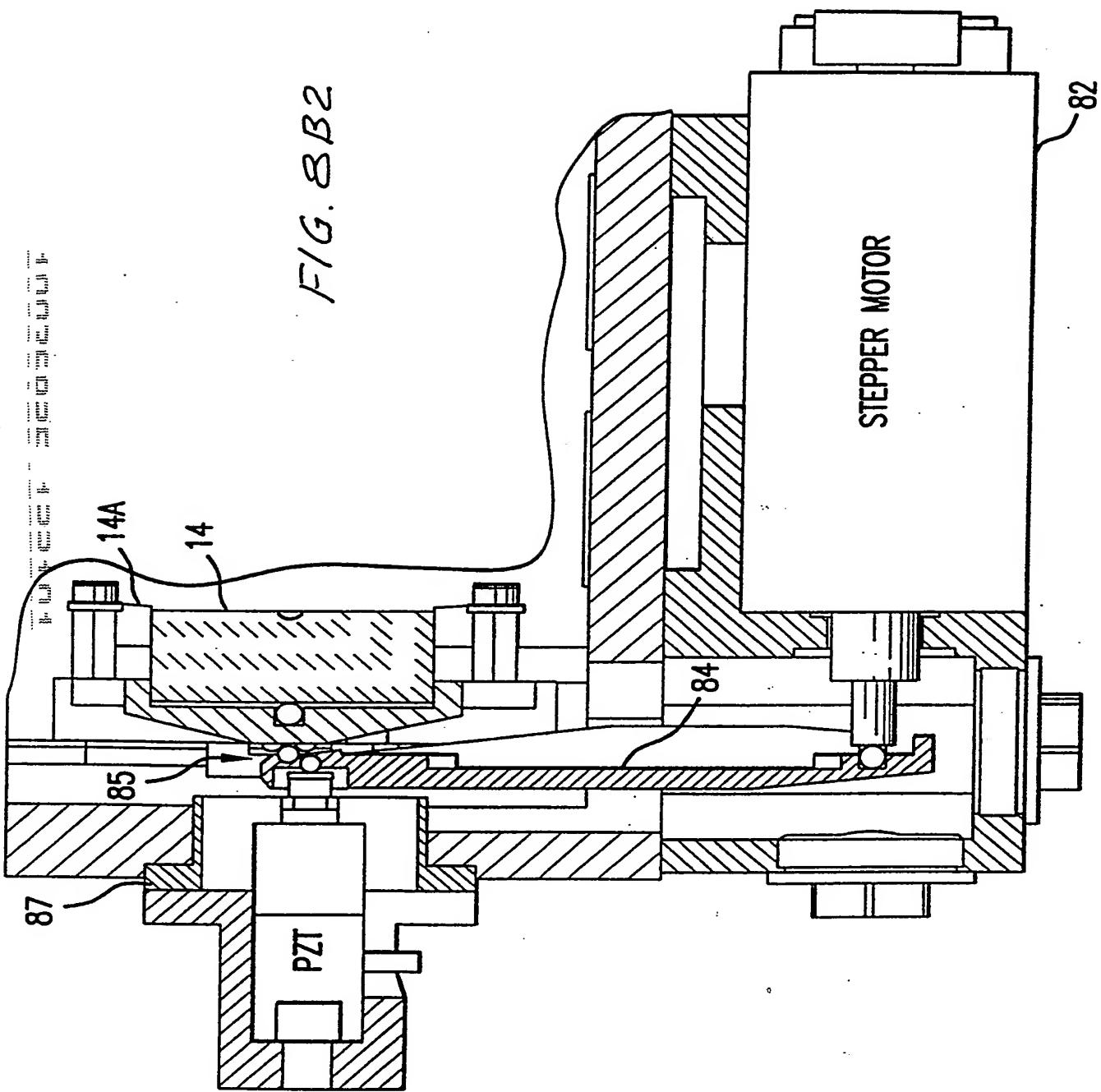
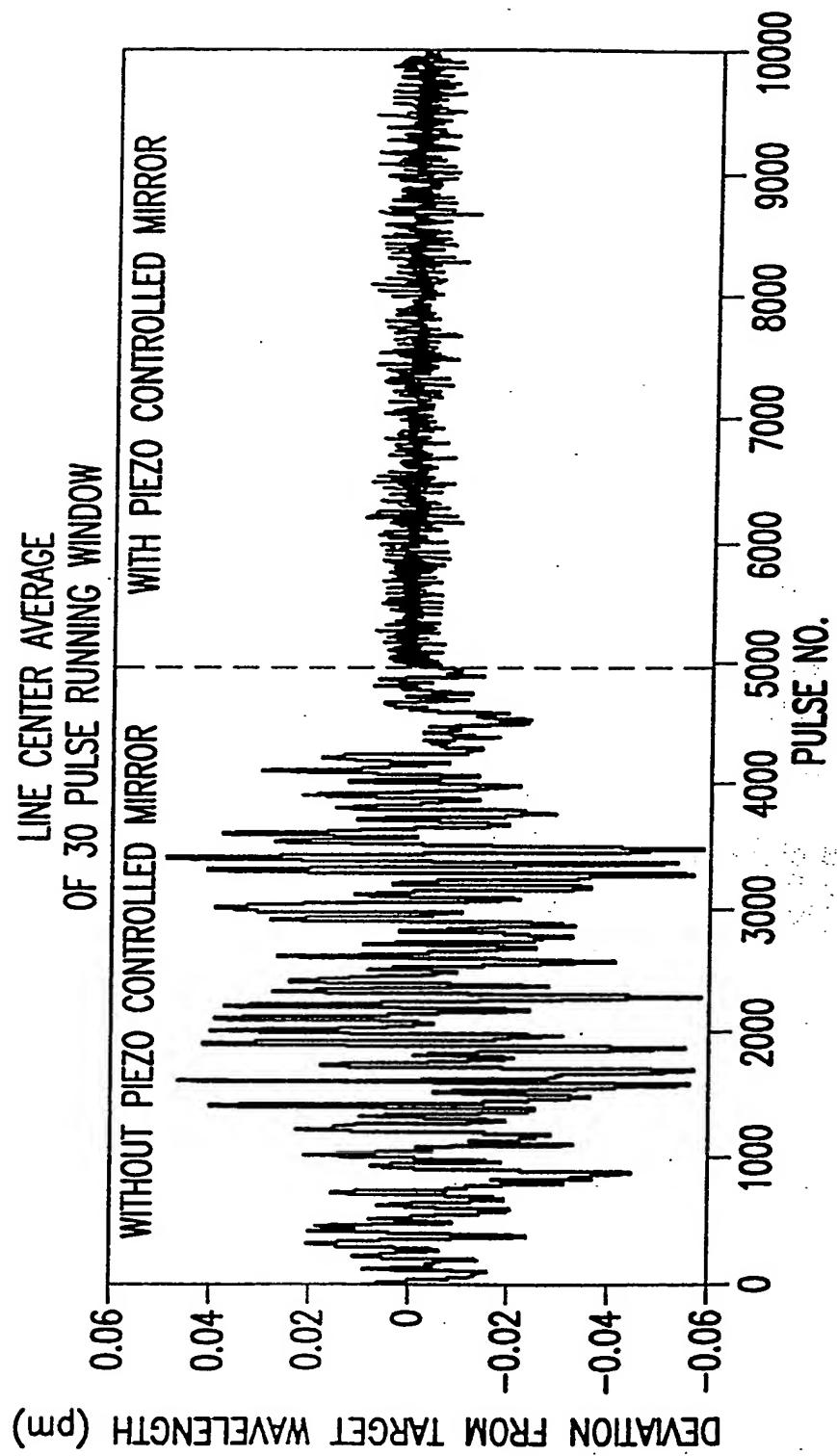


FIG. 8C



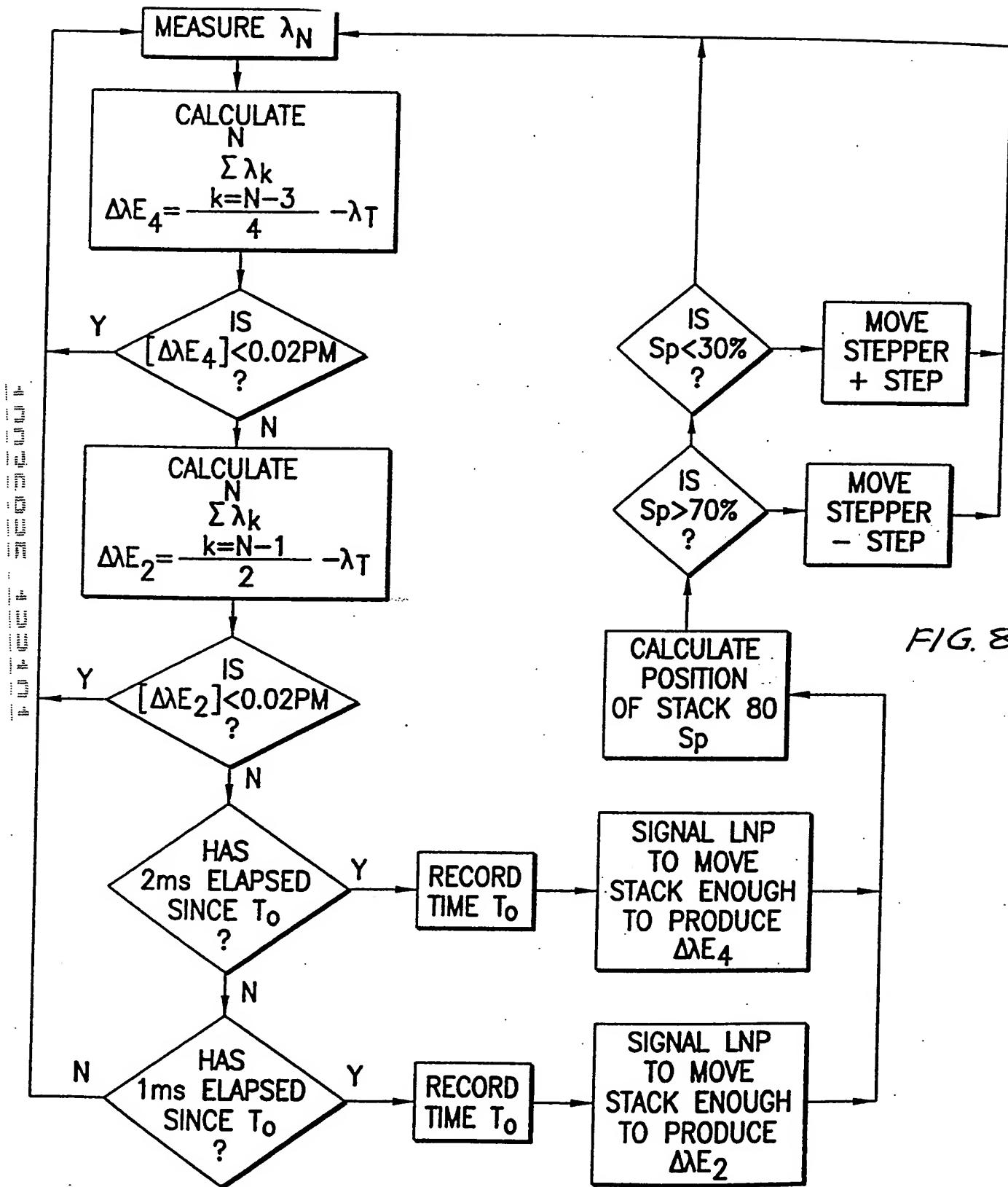
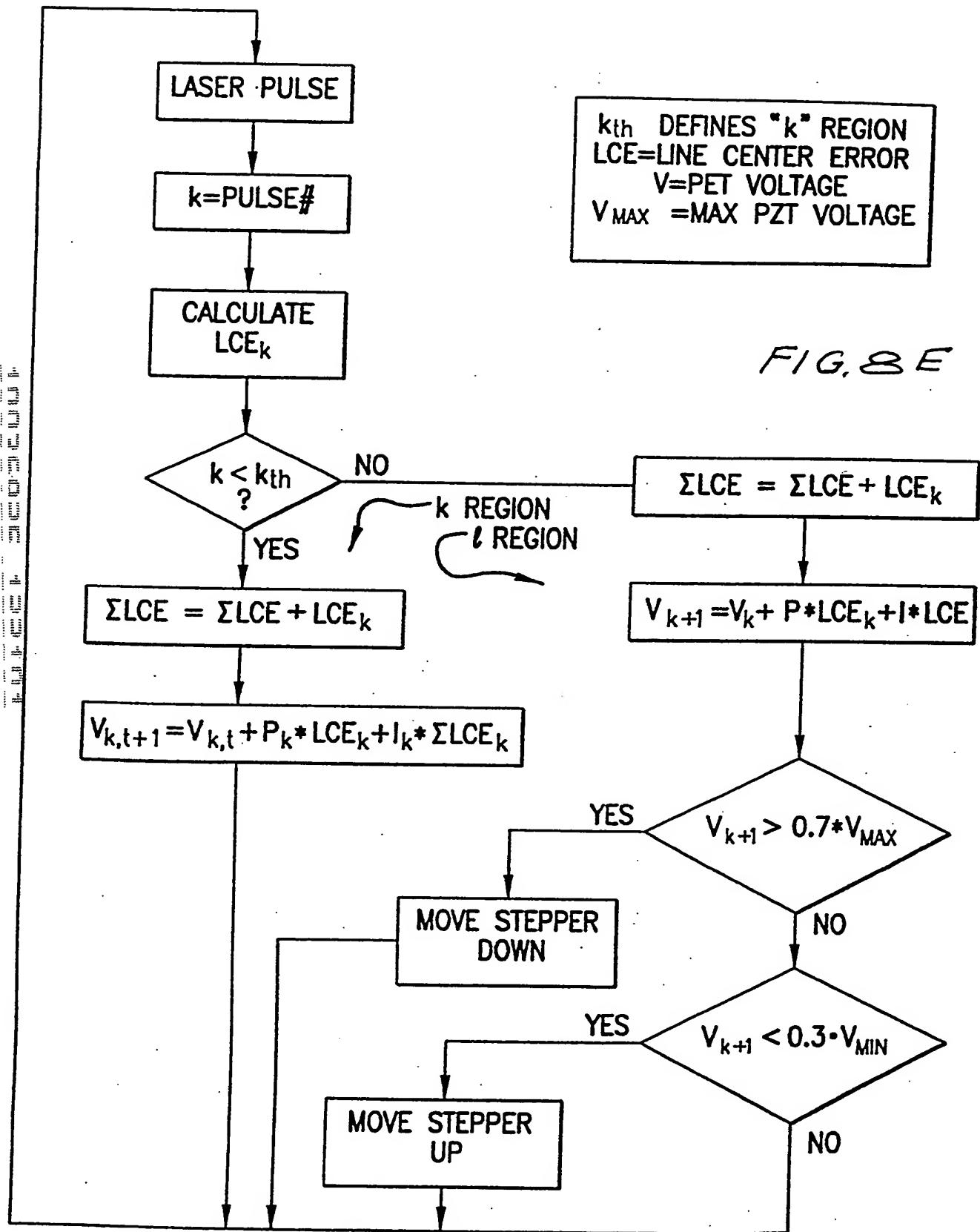


FIG. 8D



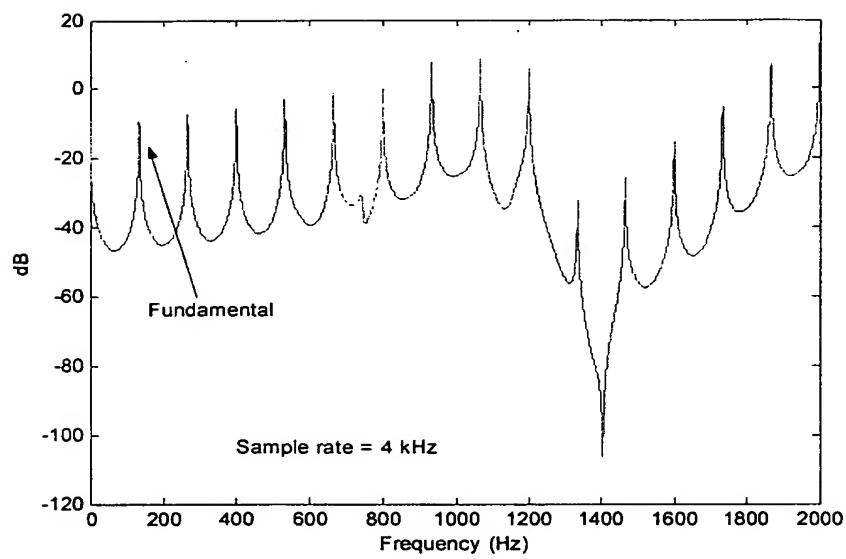


FIG. 9A

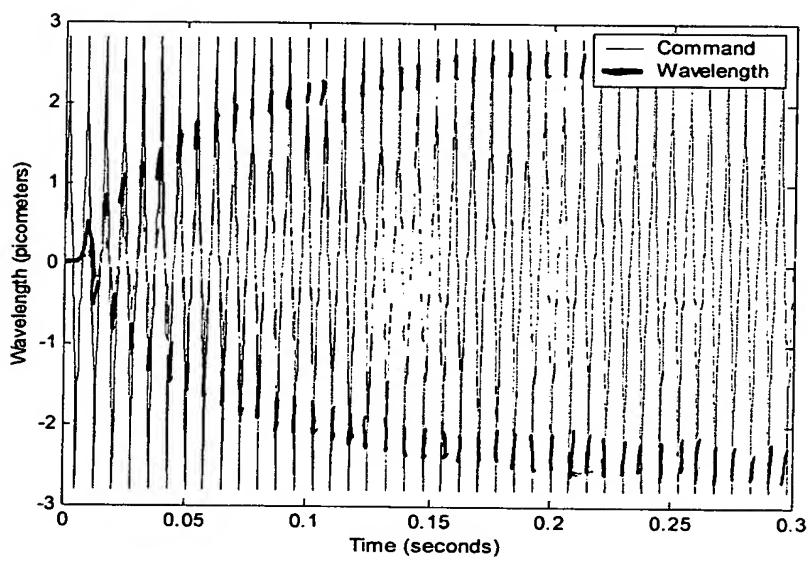


FIG 9B

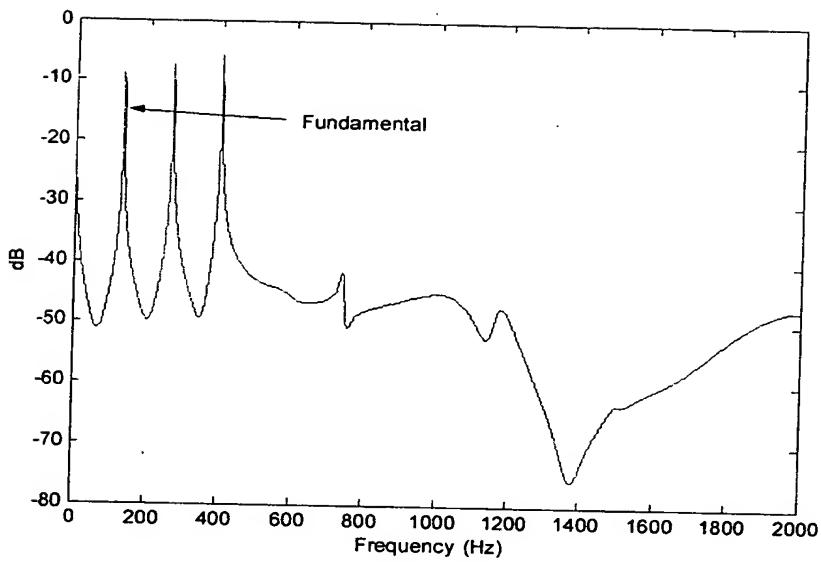


FIG. 9C

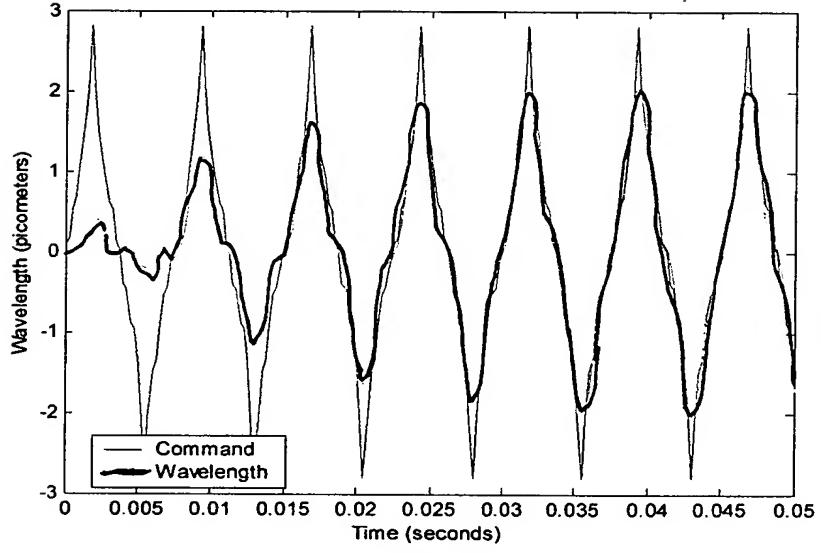


FIG. 9D

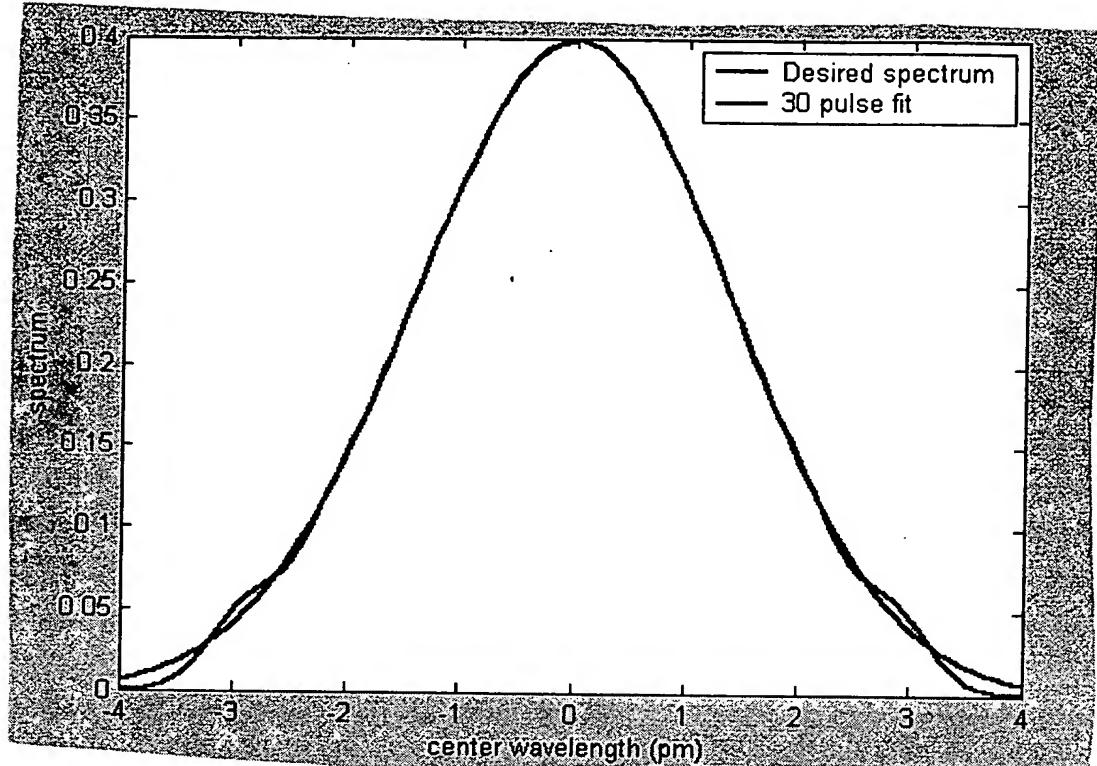


FIG. 10A

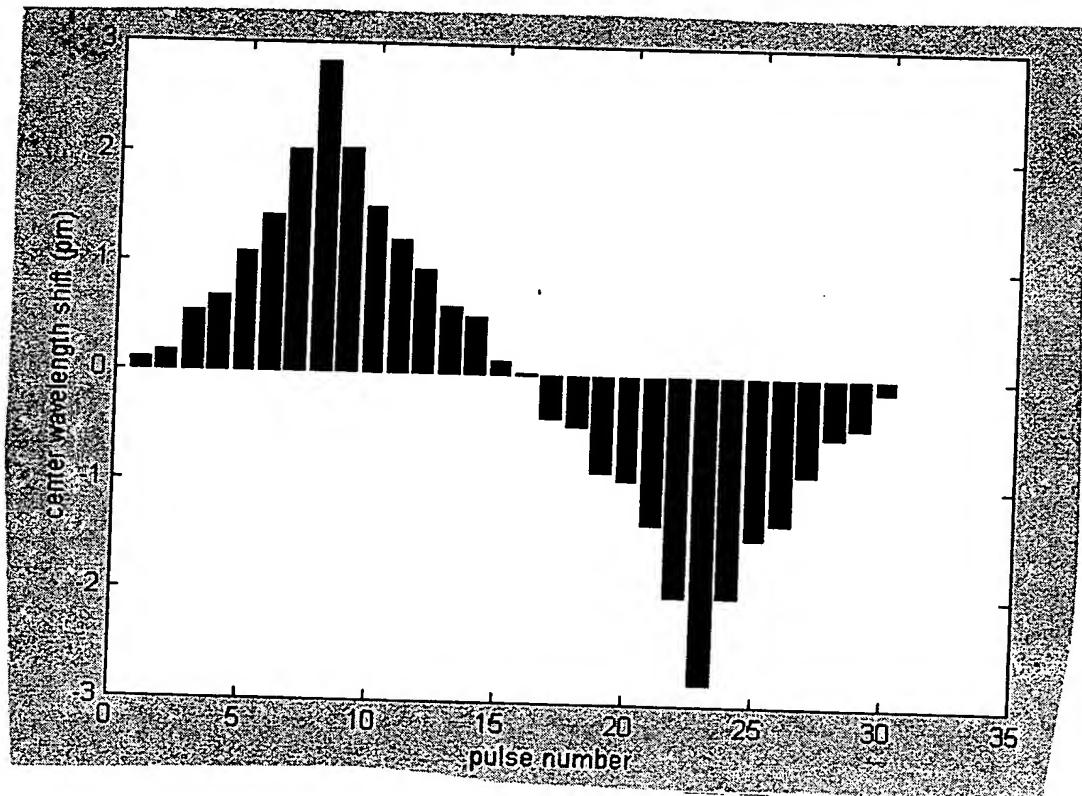


FIG. 10B

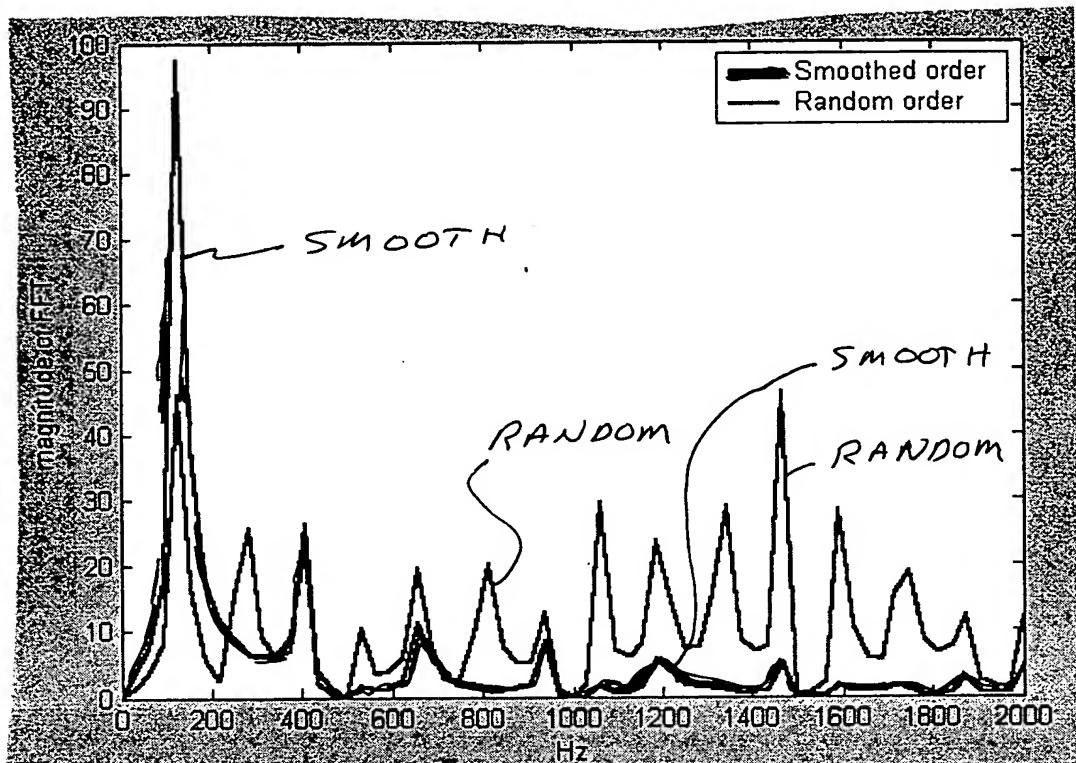


FIG. 10C

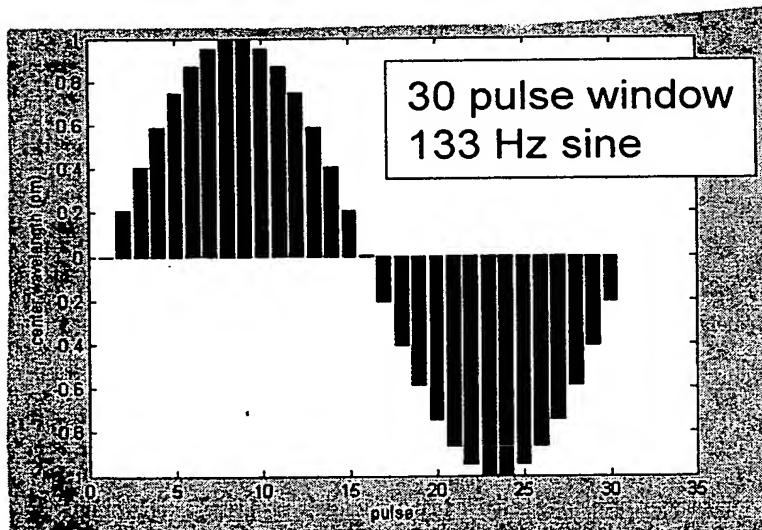


FIG. 10D

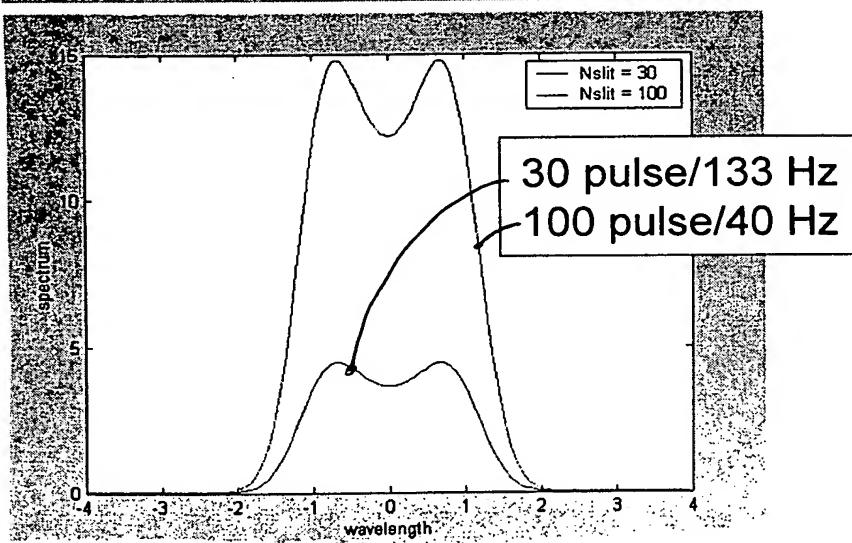


FIG. 10E

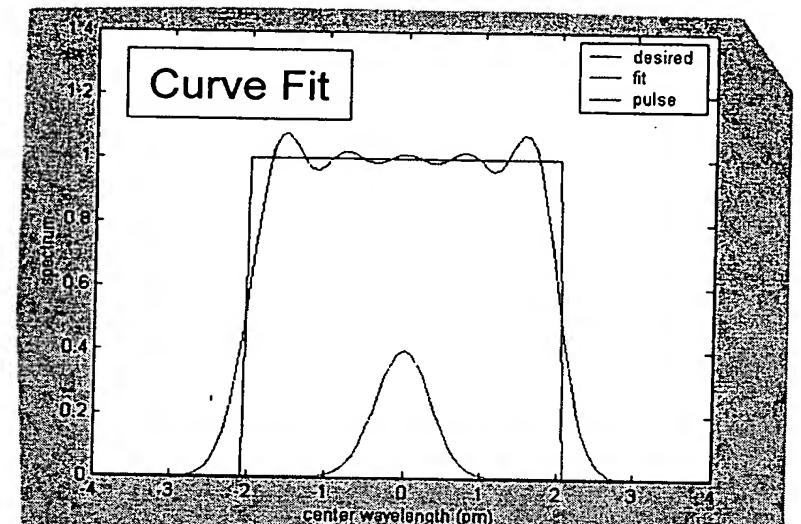


FIG. 10F

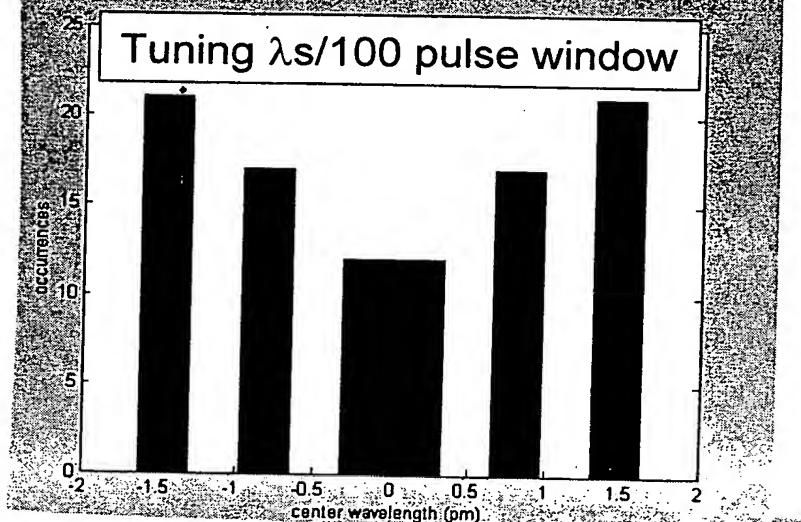


FIG. 10G

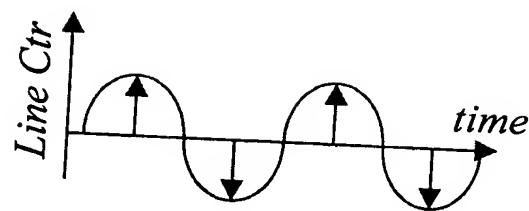


FIG. 10H

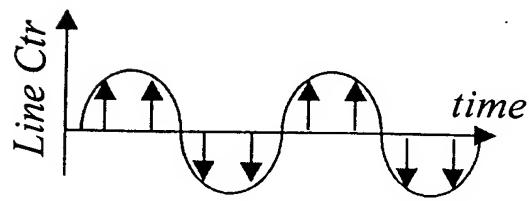


FIG. 10I